FL
Exception

BellSouth Monthly Performance Summary
Georgia 2001

				Benchmark / Analog	September Data				August Data				July Data				June Data				May Data			
					BST Measure	BST Volume	CLEC Measure	CLEC Volume	BST Measure	BST Volume	CLEC Measure	CLEC Volume	BST Measure	BST Volume	CLEC Measure	CLEC Volume	BST Measure	BST Volume	CLEC Measure	CLEC Volume	BST Measure	BST Volume	CLEC Measure	CLEC Volume
		Ordering																						
	Resale	***Reject Interval - Mechanized***																						
51, 54	A.1.4.1	O-8	Residence/GA(%)	>= 97% w in 1 hr	[redacted]	[redacted]	96.78%	3,725	[redacted]	[redacted]	93.58%	4,343	[redacted]	[redacted]	95.30%	4,407	[redacted]	[redacted]	82.53%	4,014	[redacted]	[redacted]	88.13%	4,699
51, 54	A.1.4.2	O-8	Business/GA(%)	>= 97% w in 1 hr	[redacted]	[redacted]	95.00%	180	[redacted]	[redacted]	96.73%	164	[redacted]	[redacted]	99.22%	128	[redacted]	[redacted]	89.28%	138	[redacted]	[redacted]	97.37%	114
51, 54	A.1.4.3	O-8	Design (Specials)/GA(%)	>= 97% w in 1 hr	[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]		
51, 54	A.1.4.4	O-8	PBX/GA(%)	>= 97% w in 1 hr	[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]		
51, 54	A.1.4.5	O-8	Centrex/GA(%)	>= 97% w in 1 hr	[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]		
51, 54	A.1.4.6	O-8	ISDN/GA(%)	>= 97% w in 1 hr	[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]		
	UNE	***Reject Interval - Mechanized***																						
51, 54	B.1.4.1	O-8	Switch Ports/GA(%)	>= 97% w in 1 hr	[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]		
51, 54	B.1.4.2	O-8	Local Interoffice Transport/GA(%)	>= 97% w in 1 hr	[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]	96.67%	30	[redacted]	[redacted]	100.00%	8
51, 54	B.1.4.3	O-8	Loop + Port Combinations/GA(%)	>= 97% w in 1 hr	[redacted]	[redacted]	90.44%	7,983	[redacted]	[redacted]	91.83%	8,087	[redacted]	[redacted]	73.88%	5,180	[redacted]	[redacted]	88.17%	4,507	[redacted]	[redacted]	88.21%	3,190
51, 54	B.1.4.4	O-8	Combo Other/GA(%)	>= 97% w in 1 hr	[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]		
51, 54	B.1.4.5	O-8	xDSL (ADSL, HDSL and UCL)/GA(%)	>= 97% w in 1 hr	[redacted]	[redacted]	100.00%	8	[redacted]	[redacted]	100.00%	44	[redacted]	[redacted]	100.00%	24	[redacted]	[redacted]	100.00%	47	[redacted]	[redacted]	100.00%	81
51, 54	B.1.4.6	O-8	ISDN Loop (UDN, UDC)/GA(%)	>= 97% w in 1 hr	[redacted]	[redacted]	100.00%	3	[redacted]	[redacted]	100.00%	1	[redacted]	[redacted]			[redacted]	[redacted]	100.00%	3	[redacted]	[redacted]	100.00%	2
51, 54	B.1.4.7	O-8	Line Sharing/GA(%)	>= 97% w in 1 hr	[redacted]	[redacted]	100.00%	1	[redacted]	[redacted]	100.00%	5	[redacted]	[redacted]	100.00%	3	[redacted]	[redacted]	100.00%	1	[redacted]	[redacted]		
51, 54	B.1.4.8	O-8	2W Analog Loop Design/GA(%)	>= 97% w in 1 hr	[redacted]	[redacted]	91.07%	112	[redacted]	[redacted]	77.88%	113	[redacted]	[redacted]	97.00%	100	[redacted]	[redacted]	98.10%	158	[redacted]	[redacted]	88.81%	94
51, 54	B.1.4.9	O-8	2W Analog Loop Non-Design/GA(%)	>= 97% w in 1 hr	[redacted]	[redacted]	82.50%	16	[redacted]	[redacted]	100.00%	19	[redacted]	[redacted]	100.00%	5	[redacted]	[redacted]	100.00%	5	[redacted]	[redacted]	100.00%	17
51, 54	B.1.4.10	O-8	2W Analog Loop w/INP Design/GA(%)	>= 97% w in 1 hr	[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]		
51, 54	B.1.4.11	O-8	2W Analog Loop w/INP Non-Design/GA(%)	>= 97% w in 1 hr	[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]		
51, 54	B.1.4.12	O-14	2W Analog Loop w/LNP Design/GA(%)	>= 97% w in 1 hr	[redacted]	[redacted]	100.00%	4	[redacted]	[redacted]	100.00%	7	[redacted]	[redacted]	100.00%	1	[redacted]	[redacted]			[redacted]	[redacted]	100.00%	11
51, 54	B.1.4.13	O-14	2W Analog Loop w/LNP Non-Design/GA(%)	>= 97% w in 1 hr	[redacted]	[redacted]	100.00%	2	[redacted]	[redacted]	100.00%	4	[redacted]	[redacted]	100.00%	2	[redacted]	[redacted]	0.00%	4	[redacted]	[redacted]	100.00%	28
51, 54	B.1.4.14	O-8	Other Design/GA(%)	>= 97% w in 1 hr	[redacted]	[redacted]	71.43%	14	[redacted]	[redacted]	76.19%	21	[redacted]	[redacted]	88.88%	5	[redacted]	[redacted]	96.97%	33	[redacted]	[redacted]	100.00%	10
51, 54	B.1.4.15	O-8	Other Non-Design/GA(%)	>= 97% w in 1 hr	[redacted]	[redacted]	82.42%	322	[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]	88.17%	4,887	[redacted]	[redacted]	88.21%	3,190
51, 54	B.1.4.16	O-8	INP Standalone/GA(%)	>= 97% w in 1 hr	[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]	100.00%	1
51, 54	B.1.4.17	O-14	LNP Standalone/GA(%)	>= 97% w in 1 hr	[redacted]	[redacted]	72.55%	51	[redacted]	[redacted]	97.58%	124	[redacted]	[redacted]	91.89%	37	[redacted]	[redacted]	74.83%	77	[redacted]	[redacted]	80.91%	88
	Resale	***FOC Timeliness - Mechanized***																						
85, 100	A.1.9.1	O-9	Residence/GA(%)	>= 95% w in 3 hrs	[redacted]	[redacted]	99.79%	22,827	[redacted]	[redacted]	98.49%	26,723	[redacted]	[redacted]	97.67%	23,756	[redacted]	[redacted]	97.98%	27,881	[redacted]	[redacted]	97.81%	32,648
85, 100	A.1.9.2	O-9	Business/GA(%)	>= 95% w in 3 hrs	[redacted]	[redacted]	98.35%	488	[redacted]	[redacted]	97.18%	602	[redacted]	[redacted]	100.00%	416	[redacted]	[redacted]	100.00%	422	[redacted]	[redacted]	100.00%	394
85, 100	A.1.9.3	O-9	Design (Specials)/GA(%)	>= 95% w in 3 hrs	[redacted]	[redacted]	100.00%	1	[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]		
85, 100	A.1.9.4	O-9	PBX/GA(%)	>= 95% w in 3 hrs	[redacted]	[redacted]			[redacted]	[redacted]	100.00%	1	[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]		
85, 100	A.1.9.5	O-9	Centrex/GA(%)	>= 95% w in 3 hrs	[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]		
85, 100	A.1.9.6	O-9	ISDN/GA(%)	>= 95% w in 3 hrs	[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]		
	UNE	***FOC Timeliness - Mechanized***																						
85, 100	B.1.9.1	O-9	Switch Ports/GA(%)	>= 95% w in 3 hrs	[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]		
85, 100	B.1.9.2	O-9	Local Interoffice Transport/GA(%)	>= 95% w in 3 hrs	[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]	100.00%	5	[redacted]	[redacted]	66.67%	3
85, 100	B.1.9.3	O-9	Loop + Port Combinations/GA(%)	>= 95% w in 3 hrs	[redacted]	[redacted]	97.54%	33,216	[redacted]	[redacted]	94.88%	42,488	[redacted]	[redacted]	95.90%	28,158	[redacted]	[redacted]	94.92%	20,367	[redacted]	[redacted]	95.62%	13,229
85, 100	B.1.9.4	O-9	Combo Other/GA(%)	>= 95% w in 3 hrs	[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]		
85, 100	B.1.9.5	O-9	xDSL (ADSL, HDSL and UCL)/GA(%)	>= 95% w in 3 hrs	[redacted]	[redacted]	83.67%	49	[redacted]	[redacted]	80.97%	599	[redacted]	[redacted]	84.72%	432	[redacted]	[redacted]	84.95%	297	[redacted]	[redacted]	88.81%	113
85, 100	B.1.9.6	O-9	ISDN Loop (UDN, UDC)/GA(%)	>= 95% w in 3 hrs	[redacted]	[redacted]	100.00%	13	[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]	100.00%	1	[redacted]	[redacted]	100.00%	1
85, 100	B.1.9.7	O-9	Line Sharing/GA(%)	>= 95% w in 3 hrs	[redacted]	[redacted]	100.00%	3	[redacted]	[redacted]	100.00%	4	[redacted]	[redacted]	100.00%	22	[redacted]	[redacted]	100.00%	1	[redacted]	[redacted]		
85, 100	B.1.9.8	O-9	2W Analog Loop Design/GA(%)	>= 95% w in 3 hrs	[redacted]	[redacted]	98.88%	159	[redacted]	[redacted]	98.58%	66	[redacted]	[redacted]	100.00%	36	[redacted]	[redacted]	98.43%	56	[redacted]	[redacted]	89.74%	39
85, 100	B.1.9.9	O-9	2W Analog Loop Non-Design/GA(%)	>= 95% w in 3 hrs	[redacted]	[redacted]	100.00%	108	[redacted]	[redacted]	100.00%	43	[redacted]	[redacted]	100.00%	18	[redacted]	[redacted]	100.00%	4	[redacted]	[redacted]	100.00%	12
85, 100	B.1.9.10	O-9	2W Analog Loop w/INP Design/GA(%)	>= 95% w in 3 hrs	[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]		
85, 100	B.1.9.11	O-9	2W Analog Loop w/INP Non-Design/GA(%)	>= 95% w in 3 hrs	[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]		
85, 100	B.1.9.12	O-15	2W Analog Loop w/LNP Design/GA(%)	>= 95% w in 3 hrs	[redacted]	[redacted]	94.74%	19	[redacted]	[redacted]	92.59%	27	[redacted]	[redacted]	100.00%	30	[redacted]	[redacted]	54.55%	11	[redacted]	[redacted]	45.88%	85
85, 100	B.1.9.13	O-15	2W Analog Loop w/LNP Non-Design/GA(%)	>= 95% w in 3 hrs	[redacted]	[redacted]	100.00%	2	[redacted]	[redacted]	100.00%	1	[redacted]	[redacted]	100.00%	1	[redacted]	[redacted]	100.00%	2	[redacted]	[redacted]	34.15%	41
85, 100	B.1.9.14	O-9	Other Design/GA(%)	>= 95% w in 3 hrs	[redacted]	[redacted]	100.00%	43	[redacted]	[redacted]	100.00%	32	[redacted]	[redacted]	91.67%	12	[redacted]	[redacted]	100.00%	6	[redacted]	[redacted]	75.00%	4
85, 100	B.1.9.15	O-9	Other Non-Design/GA(%)	>= 95% w in 3 hrs	[redacted]	[redacted]	99.07%	1,508	[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]	94.52%	20,367	[redacted]	[redacted]	95.62%	13,229
85, 100	B.1.9.16	O-9	INP Standalone/GA(%)	>= 95% w in 3 hrs	[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]		
85, 100	B.1.9.17	O-15	LNP Standalone/GA(%)	>= 95% w in 3 hrs	[redacted]	[redacted]	99.63%	802	[redacted]	[redacted]	100.00%	764	[redacted]	[redacted]	98.85%	887	[redacted]	[redacted]	78.58%	2,241	[redacted]	[redacted]	96.75%	3,142
	Resale	***FOC & Reject Response Completeness - Mechanized***																						
90	A.1.14.1	O-11	Residence/GA(%)	>= 95%	[redacted]	[redacted]	98.52%	26,593	[redacted]	[redacted]	99.37%	31,133	[redacted]	[redacted]	91.96%	30,631	[redacted]	[redacted]	97.93%	32,361	[redacted]	[redacted]	98.10%	38,049
90	A.1.14.2	O-11	Business/GA(%)	>= 95%	[redacted]	[redacted]	97.87%	657	[redacted]	[redacted]	97.06%	783	[redacted]	[redacted]	88.42%	798	[redacted]	[redacted]	85.00%	660	[redacted]	[redacted]	82.87%	813
90	A.1.14.3	O-11	Design (Specials)/GA(%)	>= 95%	[redacted]	[redacted]	50.00%	2	[redacted]	[redacted]			[redacted]	[redacted]	0.00%	1	[redacted]	[redacted]			[redacted]	[redacted]		
90	A.1.14.4	O-11	PBX/GA(%)	>= 95%	[redacted]	[redacted]			[redacted]	[redacted]	100.00%	1	[redacted]	[redacted]	0.00%	2	[redacted]	[redacted]			[redacted]	[redacted]		
90	A.1.14.5	O-11	Centrex/GA(%)	>= 95%	[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]		
90	A.1.14.6	O-11	ISDN/GA(%)	>= 95%	[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]		
	UNE	***FOC & Reject Response Completeness - Mechanized***																						
90	B.1.14.1	O-11	Switch Ports/GA(%)	>= 95%	[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]		
90	B.1.14.2	O-11	Local Interoffice Transport/GA(%)	>= 95%	[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]	82.11%	38	[redacted]	[redacted]	84.62%	13
90	B.1.14.3	O-11	Loop + Port Combinations/GA(%)	>= 95%	[redacted]	[redacted]	99.46%	40,817	[redacted]	[redacted]	99.35%	50,457	[redacted]	[redacted]	86.64%	38,938	[redacted]	[redacted]	93.43%	28,621	[redacted]	[redacted]	94.00%	17,467
90	B.1.14.4	O-11	Combo Other/GA(%)	>= 95%	[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]		
90	B.1.14.5	O-11	xDSL (ADSL, HDSL and UCL)/GA(%)	>= 95%	[redacted]	[redacted]	88.66%	89	[redacted]	[redacted]	82.53%	174	[redacted]	[redacted]	84.45%	9,724	[redacted]	[redacted]	88.58%	1,223	[redacted]	[redacted]	84.34%	258
90	B.1.14.6	O-11	ISDN Loop (UDN, UDC)/GA(%)	>= 95%	[redacted]	[redacted]	100.00%	16	[redacted]	[redacted]	100.00%	1	[redacted]	[redacted]	0.00%	1	[redacted]	[redacted]	100.00%	4	[redacted]	[redacted]	100.00%	3
90	B.1.14.7	O-11	Line Sharing/GA(%)	>= 90%	[redacted]	[redacted]	100.00%	4	[redacted]	[redacted]	90.00%	10	[redacted]	[redacted]	71.43%	35	[redacted]	[redacted]	25.00%	8	[redacted]	[redacted]		
90	B.1.14.8	O-11	2W Analog Loop Design/GA(%)	>= 95%	[redacted]	[redacted]	99.26%	271	[redacted]	[redacted]	93.46%	214	[redacted]	[redacted]	85.53%	159	[redacted]	[redacted]	83.98%	256	[redacted]	[redacted]	85.26%	156
90	B.1.14.9	O-11	2W Analog Loop Non-Design/GA(%)	>= 95%	[redacted]	[redacted]	99.17%	120	[redacted]	[redacted]	98.88%	84	[redacted]	[redacted]	74.19%	31	[redacted]	[redacted]	69.23%	13	[redacted]	[redacted]	82.86%	35
90	B.1.14.10	O-11	2W Analog Loop w/INP Design/GA(%)	>= 95%	[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]		
90	B.1.14.11	O-11	2W Analog Loop w/INP Non-Design/GA(%)	>= 95%	[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]		
90	B.1.14.12	O-11	2W Analog Loop w/LNP Design/GA(%)	>= 95%	[redacted]	[redacted]	100.00%	95	[redacted]	[redacted]	100.00%	80	[redacted]	[redacted]	100.00%	136	[redacted]	[redacted]	100.00%	36	[redacted]	[redacted]	100.00%	92
90	B.1.14.13	O-11	2W Analog Loop w/LNP Non-Design/GA(%)	>= 95%	[redacted]	[redacted]	100.00%	145	[redacted]	[redacted]	100.00%	134	[redacted]	[redacted]	100.00%	92	[redacted]	[redacted]	100.00%	79	[redacted]	[redacted]	100.00%	39
90	B.1.14.14	O-11	Other Design/GA(%)	>= 95%	[redacted]	[redacted]	100.00%	55	[redacted]	[redacted]	98.08%	52	[redacted]	[redacted]	53.13%	32	[redacted]	[redacted]	92.86%	42	[redacted]	[redacted]	87.50%	16
90	B.1.14.15	O-11	Other Non-Design/GA(%)	>= 95%	[redacted]	[redacted]	98.38%	1,771	[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]	93.43%	28,621	[redacted]	[redacted]	94.00%	17,467
90	B.1.14.16	O-11	INP Standalone/GA(%)	>= 95%	[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]		
90	B.1.14.17	O-11	LNP Standalone/GA(%)	>= 95%	[redacted]	[redacted]	100.00%	238	[redacted]	[redacted]	100.00%	286	[redacted]	[redacted]	100.00%	233	[redacted]	[redacted]	100.00%	329	[redacted]	[redacted]	100.00%	329
	Resale	***FOC & Reject Response Completeness - Non-Mechanized***																						
72, 105, 117	A.1.16.1	O-11	Residence/GA(%)	>= 95%	[redacted]	[redacted]	95.21%	167	[redacted]	[redacted]	98.43%	252	[redacted]	[redacted]	83.81%	228	[redacted]	[redacted]	94.21%	311	[redacted]	[redacted]	94.09%	423
72, 105, 117	A.1.16.2	O-11	Business/GA(%)	>= 95%	[redacted]	[redacted]	98.30%	270	[redacted]	[redacted]	95.95%	494	[redacted]	[redacted]	84.47%	416	[redacted]	[redacted]	93.46%	367	[redacted]	[redacted]	95.59%	340
72, 105, 117	A.1.16.3	O-11	Design (Specials)/GA(%)	>= 95%	[redacted]	[redacted]	92.16%	204	[redacted]	[redacted]	90.42%	187	[redacted]	[redacted]	92.86%	56	[redacted]	[redacted]	89.71%	68	[redacted]	[redacted]	94.59%	74
72, 105, 117	A.1.16.4	O-11	PBX/GA(%)	>= 95%	[redacted]	[redacted]	90.00%	20	[redacted]	[redacted]	88.57%	35	[redacted]	[redacted]	97.14%	35	[redacted]	[redacted]	84.44%	18	[redacted]	[redacted]	94.74%	19
72, 105, 117	A.1.16.5	O-11	Centrex/GA(%)	>= 95%	[redacted]	[redacted]	97.80%	24	[redacted]	[redacted]	86.96%	23	[redacted]	[redacted]	82.61%	23	[redacted]	[redacted]	91.67%	12	[redacted]	[redacted]	89.74%	39
72, 105, 117	A.1.16.6	O-11	ISDN/GA(%)	>= 95%	[redacted]	[redacted]	93.33%	45	[redacted]	[redacted]	90.00%	40	[redacted]	[redacted]	93.94%	33	[redacted]	[redacted]	89.36%	47	[redacted]	[redacted]	92.45%	53
	UNE	***FOC & Reject Response Completeness - Non-Mechanized***																						
72, 105, 117	B.1.16.1	O-11	Switch Ports/GA(%)	>= 95%	[redacted]	[redacted]	100.00%	1	[redacted]	[redacted]	100.00%	1	[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]		
72, 105, 117	B.1.16.2	O-11	Local Interoffice Transport/GA(%)	>= 95%	[redacted]	[redacted]	100.00%	19	[redacted]	[redacted]	100.00%	13	[redacted]	[redacted]			[redacted]	[redacted]	98.20%	158	[redacted]	[redacted]	98.68%	152
72, 105, 117	B.1.16.3	O-11	Loop + Port Combinations/GA(%)	>= 90%	[redacted]	[redacted]	98.66%	746	[redacted]	[redacted]	98.29%	2,278	[redacted]	[redacted]	92.63%	339	[redacted]	[redacted]	98.80%	1,211	[redacted]	[redacted]	97.13%	1,328
72, 105, 117	B.1.16.4	O-11	Combo Other/GA(%)	>= 95%	[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]		
72, 105, 117	B.1.16.5	O-11	xDSL (ADSL, HDSL and UCL)/GA(%)	>= 95%	[redacted]	[redacted]	98.17%	273	[redacted]	[redacted]	83.18%	285	[redacted]	[redacted]	80.00%	10	[redacted]	[redacted]	100.00%	6	[redacted]	[redacted]	90.00%	30
72, 105, 117	B.1.16.6	O-11	ISDN Loop (UDN, UDC)/GA(%)	>= 95%	[redacted]	[redacted]	98.64%	441	[redacted]	[redacted]	97.80%	733	[redacted]	[redacted]	100.00%	92	[redacted]	[redacted]	84.03%	119	[redacted]	[redacted]	100.00%	285
72, 105, 117	B.1.16.7	O-11	Line Sharing/GA(%)	>= 95%	[redacted]	[redacted]	98.80%	87	[redacted]	[redacted]	91.86%	86	[redacted]	[redacted]	96.83%	192	[redacted]	[redacted]	94.30%	193	[redacted]	[redacted]	95.71%	140
72, 105, 117	B.1.16.8	O-11	2W Analog Loop Design/GA(%)	>= 95%	[redacted]	[redacted]	95.24%	185	[redacted]	[redacted]	94.58%	166	[redacted]	[redacted]	98.31%	59	[redacted]	[redacted]	93.02%	86	[redacted]	[redacted]	100.00%	63
72, 105, 117	B.1.16.9	O-11	2W Analog Loop Non-Design/GA(%)	>= 95%	[redacted]	[redacted]	93.90%	1,017	[redacted]	[redacted]	91.75%	1,381	[redacted]	[redacted]	94.88%	2,173	[redacted]	[redacted]	94.20%	2,276	[redacted]	[redacted]	98.06%	2,892
72, 105, 117	B.1.16.10	O-11	2W Analog Loop w/INP Design/GA(%)	>= 95%	[redacted]	[redacted]	100.00%	14	[redacted]	[redacted]	88.75%	32	[redacted]	[redacted]	66.67%	3	[redacted]	[redacted]			[redacted]	[redacted]		
72, 105, 117	B.1.16.11	O-11	2W Analog Loop w/INP Non-Design/GA(%)	>= 95%	[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]		
72, 105, 117	B.1.16.12	O-11	2W Analog Loop w/LNP Design/GA(%)	>= 95%	[redacted]	[redacted]	100.00%	8	[redacted]	[redacted]	100.00%	39	[redacted]	[redacted]	100.00%	80	[redacted]	[redacted]	100.00%	41	[redacted]	[redacted]	100.00%	29
72, 105, 117	B.1.16.13	O-11	2W Analog Loop w/LNP Non-Design/GA(%)	>= 95%	[redacted]	[redacted]	99.54%	216	[redacted]	[redacted]	98.78%	420	[redacted]	[redacted]	98.83%	342	[redacted]	[redacted]	99.71%	344	[redacted]	[redacted]	99.61%	808
72, 105, 117	B.1.16.14	O-11	Other Design/GA(%)	>= 95%	[redacted]	[redacted]	97.00%	267	[redacted]	[redacted]	96.43%	308	[redacted]	[redacted]	98.76%	161	[redacted]	[redacted]	90.97%	277	[redacted]	[redacted]	99.52%	417
72, 105, 117	B.1.16.15	O-11	Other Non-Design/GA(%)	>= 95%	[redacted]	[redacted]	97.30%	1,443	[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]	98.80%	1,212	[redacted]	[redacted]	97.13%	1,326
72, 105, 117	B.1.16.16	O-11	INP Standalone/GA(%)	>= 95%	[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]			[redacted]	[redacted]		

FL
Exception

BellSouth Monthly Performance Summary
Georgia 2001

Report	Measure No.	SQM	Measure	Benchmark / Analog	Sept. BST Measure	Sept. BST Volume	Sept. CLEC Measure	Sept. CLEC Volume	Aug. BST Measure	Aug. BST Volume	Aug. CLEC Measure	Aug. CLEC Volume	July BST Measure	July BST Volume	July CLEC Measure	July CLEC Volume	June BST Measure	June BST Volume	June CLEC Measure	June CLEC Volume	May BST Measure	May BST Volume	May CLEC Measure	May CLEC Volume
72, 105, 117	B.1.16.17	O-11	LNP Standalone/GA(%)	>= 95%			98.88%	919			98.72%	1,066			98.90%	978			100.00%	880			98.80%	994
			Provisioning																					
Resale			**% Provisioning Troubles within 30 Days**																					
76, 84, 112, 116	A.2.12.1.1.1	P-9	Residence/<10 circuits/Dispatch/GA(%)	Res	10.87%	48,493	11.40%	1,248	10.54%	31,424	8.32%	1,137	11.19%	30,238	9.53%	1,238	11.80%	29,716	10.77%	1,291	7.87%	40,700	12.27%	1,475
76, 84, 112, 116	A.2.12.1.1.2	P-9	Residence/<10 circuits/Non-Dispatch/GA(%)	Res	4.65%	450,156	4.84%	23,743	4.27%	413,819	3.72%	22,050	4.33%	432,425	3.74%	24,733	4.35%	441,728	3.39%	20,050	4.18%	441,906	4.18%	24,518
76, 84, 112, 116	A.2.12.1.2.1	P-9	Residence/>=10 circuits/Dispatch/GA(%)	Res	8.70%	69	0.00%	1	14.58%	48	0.00%	1	7.14%	42	20.00%	5	8.00%	25	25.00%	4	13.33%	45	0.00%	1
76, 84, 112, 116	A.2.12.1.2.2	P-9	Residence/>=10 circuits/Non-Dispatch/GA(%)	Res	0.00%	1			0.00%	2							0.00%	1			0.00%	4		
76, 84, 112, 116	A.2.12.2.1.1	P-9	Business/<10 circuits/Dispatch/GA(%)	Bus	3.10%	25,428	8.74%	282	3.12%	21,273	7.35%	204	3.16%	21,759	8.08%	215	3.71%	21,984	10.50%	238	2.45%	30,458	5.95%	370
76, 84, 112, 116	A.2.12.2.1.2	P-9	Business/<10 circuits/Non-Dispatch/GA(%)	Bus	5.24%	23,207	8.51%	753	5.08%	21,518	4.88%	625	5.10%	22,612	4.14%	724	5.31%	24,627	8.29%	779	4.20%	25,738	3.92%	1,172
76, 84, 112, 116	A.2.12.2.2.1	P-9	Business/>=10 circuits/Dispatch/GA(%)	Bus	9.09%	143	0.00%	1	9.92%	121	0.00%	2	14.81%	108	0.00%	1	9.09%	110	33.33%	6	12.70%	126	0.00%	4
76, 84, 112, 116	A.2.12.2.2.2	P-9	Business/>=10 circuits/Non-Dispatch/GA(%)	Bus	9.09%	11	0.00%	2	6.67%	15	0.00%	1	0.00%	13	0.00%	1	2.86%	35	0.00%	9	0.00%	101	0.00%	3
76, 84, 112, 116	A.2.12.3.1.1	P-9	Design (Specials)/<10 circuits/Dispatch/GA(%)	Design	5.89%	1,880	0.00%	97	5.32%	3,179	0.00%	62	5.53%	3,088	0.00%	175	6.29%	3,436	0.00%	16	3.37%	5,455	0.00%	68
76, 84, 112, 116	A.2.12.3.1.2	P-9	Design (Specials)/<10 circuits/Non-Dispatch/GA(%)	Design	6.97%	134	0.00%	8	5.26%	133	0.00%	4	0.74%	2,586	0.74%	408	1.49%	134	0.00%	6	0.48%	207	0.00%	22
76, 84, 112, 116	A.2.12.3.2.1	P-9	Design (Specials)/>=10 circuits/Dispatch/GA(%)	Design	0.00%	59			0.00%	49			0.00%	80	0.00%	1	0.00%	42			0.00%	61		
76, 84, 112, 116	A.2.12.3.2.2	P-9	Design (Specials)/>=10 circuits/Non-Dispatch/GA(%)	Design									0.00%	35							0.00%	2		
76, 84, 112, 116	A.2.12.4.1.1	P-9	PBX/<10 circuits/Dispatch/GA(%)	PBX	3.51%	57	0.00%	7	0.00%	49	0.00%	6	0.00%	25	100.00%	1	6.35%	63	0.00%	2	1.14%	88	0.00%	6
76, 84, 112, 116	A.2.12.4.1.2	P-9	PBX/<10 circuits/Non-Dispatch/GA(%)	PBX	1.75%	228	7.69%	13	8.42%	238	0.00%	14	0.47%	214	0.00%	13	3.06%	196	0.00%	21	1.79%	392	5.00%	20
76, 84, 112, 116	A.2.12.4.2.1	P-9	PBX/>=10 circuits/Dispatch/GA(%)	PBX	0.00%	2	0.00%	1	0.00%	1	0.00%	1			0.00%	1					0.00%	3		
76, 84, 112, 116	A.2.12.4.2.2	P-9	PBX/>=10 circuits/Non-Dispatch/GA(%)	PBX	0.00%	42	12.50%	8	2.08%	48	0.00%	5	0.00%	43	0.00%	5	5.88%	51	22.22%	9	1.69%	59	0.00%	4
76, 84, 112, 116	A.2.12.5.1.1	P-9	Centrex/<10 circuits/Dispatch/GA(%)	Centrex	1.91%	1,360	0.00%	2	2.24%	1,204	0.00%	3	1.06%	1,226	0.00%	10	0.99%	1,215	9.09%	11	2.07%	581	0.00%	13
76, 84, 112, 116	A.2.12.5.1.2	P-9	Centrex/<10 circuits/Non-Dispatch/GA(%)	Centrex	1.80%	1,945	7.14%	28	6.13%	1,550	0.00%	20	0.00%	1,790	0.00%	25	1.30%	2,250	4.17%	48	2.70%	927	4.56%	22
76, 84, 112, 116	A.2.12.5.2.1	P-9	Centrex/>=10 circuits/Dispatch/GA(%)	Centrex	3.33%	60	0.00%	1	6.90%	58			3.57%	56			0.00%	45			9.09%	22	0.00%	1
76, 84, 112, 116	A.2.12.5.2.2	P-9	Centrex/>=10 circuits/Non-Dispatch/GA(%)	Centrex	1.27%	316	0.00%	10	0.86%	116	0.00%	4	0.00%	126	0.00%	8	3.66%	164	0.00%	12	2.07%	193	0.00%	11
76, 84, 112, 116	A.2.12.6.1.1	P-9	ISDN/<10 circuits/Dispatch/GA(%)	ISDN	1.73%	811	0.00%	10	1.96%	767	0.00%	10	0.50%	808	0.00%	7	0.40%	742	0.00%	9	0.00%	15		
76, 84, 112, 116	A.2.12.6.1.2	P-9	ISDN/<10 circuits/Non-Dispatch/GA(%)	ISDN	0.92%	1,195	0.00%	20	0.25%	805	0.00%	24	0.00%	547	0.00%	25	0.00%	818	0.00%	28	0.00%	48	0.00%	3
76, 84, 112, 116	A.2.12.6.2.1	P-9	ISDN/>=10 circuits/Dispatch/GA(%)	ISDN	0.00%	22			7.69%	13			0.00%	11			0.00%	6						
76, 84, 112, 116	A.2.12.6.2.2	P-9	ISDN/>=10 circuits/Non-Dispatch/GA(%)	ISDN	0.00%	72	0.00%	3	0.00%	108	0.00%	3					0.00%	2						
UNE			**% Provisioning Troubles within 30 Days**																					
76, 84, 112, 116	B.2.19.1.1.1	P-9	Switch Ports/<10 circuits/Dispatch/GA(%)	R&B (POTS)	7.90%	85,921			7.54%	52,887			7.83%	51,987			8.24%	51,700			5.56%	71,159		
76, 84, 112, 116	B.2.19.1.1.2	P-9	Switch Ports/<10 circuits/Non-Dispatch/GA(%)	R&B (POTS)	4.88%	473,365			4.31%	435,136			4.36%	454,937	0.00%	4	4.40%	488,365	0.00%	3	4.16%	467,632		
76, 84, 112, 116	B.2.19.1.2.1	P-9	Switch Ports/>=10 circuits/Dispatch/GA(%)	R&B (POTS)	8.96%	212			11.24%	169			12.67%	150			8.89%	135			12.87%	171		
76, 84, 112, 116	B.2.19.1.2.2	P-9	Switch Ports/>=10 circuits/Non-Dispatch/GA(%)	R&B (POTS)	8.33%	12			5.88%	17			0.00%	13			2.78%	36			0.00%	105		
76, 84, 112, 116	B.2.19.2.1.1	P-9	Local Interoffice Transport/<10 circuits/Dispatch/GA(%)	DS1/DS3	8.71%	2,340			0.23%	1,704	0.00%	15	8.61%	1,382	22.22%	9	8.17%	1,640	0.00%	4	7.73%	1,759	0.00%	8
76, 84, 112, 116	B.2.19.2.1.2	P-9	Local Interoffice Transport/<10 circuits/Non-Dispatch/GA(%)	DS1/DS3									0.00%	1										
76, 84, 112, 116	B.2.19.2.2.1	P-9	Local Interoffice Transport/>=10 circuits/Dispatch/GA(%)	DS1/DS3																				
76, 84, 112, 116	B.2.19.2.2.2	P-9	Local Interoffice Transport/>=10 circuits/Non-Dispatch/GA(%)	DS1/DS3																				
76, 84, 112, 116	B.2.19.3.1.1	P-9	Loop + Port Combinations/<10 circuits/Dispatch/GA(%)	R&B	7.74%	67,666	4.87%	1,114	7.37%	54,316	5.20%	808	7.82%	53,663	4.26%	1,032	8.01%	53,418	5.52%	779	5.52%	71,773	6.18%	744
76, 84, 112, 116	B.2.19.3.1.2	P-9	Loop + Port Combinations/<10 circuits/Non-Dispatch/GA(%)	R&B	4.66%	476,013	2.02%	48,113	4.29%	437,226	1.86%	37,680	4.34%	457,412	2.28%	23,066	4.38%	468,517	2.63%	16,465	4.16%	466,876	3.27%	9,402
76, 84, 112, 116	B.2.19.3.1.3	P-9	Loop + Port Combinations/<10 circuits/Switch Based Orders/GA(%)	R&B	4.67%	251,364	2.28%	8,260	4.42%	186,532	2.40%	6,507												
76, 84, 112, 116	B.2.19.3.1.4	P-9	Loop + Port Combinations/<10 circuits/Dispatch In/GA(%)	R&B	4.65%	224,646	1.96%	38,653	4.12%	209,872	1.85%	31,162												
76, 84, 112, 116	B.2.19.3.2.1	P-9	Loop + Port Combinations/>=10 circuits/Dispatch/GA(%)	R&B	7.66%	274	0.00%	6	10.09%	226	25.00%	4	10.14%	207	40.00%	5	6.67%	180	42.86%	7	12.37%	194	11.11%	9
76, 84, 112, 116	B.2.19.3.2.2	P-9	Loop + Port Combinations/>=10 circuits/Non-Dispatch/GA(%)	R&B	1.35%	370	0.00%	2	1.67%	180	25.00%	4	0.00%	183	0.00%	2	3.98%	251	0.00%	1	1.42%	353	0.00%	9
76, 84, 112, 116	B.2.19.3.2.3	P-9	Loop + Port Combinations/>=10 circuits/Switch Based Orders/GA(%)	R&B	1.59%	63	0.00%	1	1.85%	54	25.00%	4												
76, 84, 112, 116	B.2.19.3.2.4	P-9	Loop + Port Combinations/>=10 circuits/Dispatch In/GA(%)	R&B	1.30%	308	0.00%	1	1.59%	126														
76, 84, 112, 116	B.2.19.4.1.1	P-9	Combo Other/<10 circuits/Dispatch/GA(%)	R&B&D - Disp	7.65%	89,861			7.23%	57,877			7.44%	57,723			7.89%	57,156			5.30%	77,290		
76, 84, 112, 116	B.2.19.4.1.4	P-9	Combo Other/<10 circuits/Dispatch In/GA(%)	R&B&D - Disp																				
76, 84, 112, 116	B.2.19.4.2.1	P-9	Combo Other/>=10 circuits/Dispatch/GA(%)	R&B&D - Disp	5.93%	364			8.28%	290			7.58%	277			5.26%	228			9.34%	257		
76, 84, 112, 116	B.2.19.4.2.4	P-9	Combo Other/>=10 circuits/Dispatch In/GA(%)	R&B&D - Disp																				
76, 84, 112, 116	B.2.19.5.1.1	P-9	xDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/GA(%)	ADSL to Retail	9.70%	12,006	2.78%	216	11.22%	12,188	5.49%	255	8.48%	12,869	3.17%	410	6.98%	13,124	6.59%	425	2.43%	11,026	5.41%	666
76, 84, 112, 116	B.2.19.5.1.2	P-9	xDSL (ADSL, HDSL and UCL)/<10 circuits/Non-Dispatch/GA(%)	ADSL to Retail	11.45%	297			15.07%	345			6.78%	901			9.87%	557			11.90%	482		
76, 84, 112, 116	B.2.19.5.2.1	P-9	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Dispatch/GA(%)	ADSL to Retail	15.38%	13			10.53%	19			22.22%	9			0.00%	8			0.00%	6		
76, 84, 112, 116	B.2.19.5.2.2	P-9	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/GA(%)	ADSL to Retail																				
76, 84, 112, 116	B.2.19.6.1.1	P-9	UNE ISDN/<10 circuits/Dispatch/GA(%)	ISDN - BRI	1.86%	483	8.50%	341	1.57%	447	8.00%	250	0.79%	505	9.48%	401	0.56%	532	11.36%	352	2.11%	615	5.11%	704
76, 84, 112, 116	B.2.19.6.1.2	P-9	UNE ISDN/<10 circuits/Non-Dispatch/GA(%)	ISDN - BRI	1.73%	520			0.25%	404			0.00%	501			0.00%	753			0.85%	587		
76, 84, 112, 116	B.2.19.6.2.1	P-9	UNE ISDN/>=10 circuits/Dispatch/GA(%)	ISDN - BRI	0.00%	1							0.00%	1										
76, 84, 112, 116	B.2.19.6.2.2	P-9	UNE ISDN/>=10 circuits/Non-Dispatch/GA(%)	ISDN - BRI					0.00%	1											0.00%	11		
76, 84, 112, 116	B.2.19.7.1.1	P-9	Line Sharing/<10 circuits/Dispatch/GA(%)	ADSL to Retail	9.70%	12,006			11.22%	12,188	0.00%	6	8.48%	12,869	0.00%	3	6.98%	13,124			2.43%	11,026	0.00%	1
76, 84, 112, 116	B.2.19.7.1.2	P-9	Line Sharing/<10 circuits/Non-Dispatch/GA(%)	ADSL to Retail	11.45%	297	6.45%	31	15.07%	345	1.54%	65	8.78%	901	10.00%	70	9.87%	557	0.00%	28	11.90%	482	0.00%	60
76, 84, 112, 116	B.2.19.7.2.1	P-9	Line Sharing/>=10 circuits/Dispatch/GA(%)	ADSL to Retail	15.38%	13			10.53%	19			22.22%	9			0.00%	8			0.00%	6		
76, 84, 112, 116	B.2.19.7.2.2	P-9	Line Sharing/>=10 circuits/Non-Dispatch/GA(%)	ADSL to Retail																				
76, 84, 112, 116	B.2.19.8.1.1	P-9	2W Analog Loop Design/<10 circuits/Dispatch/GA(%)	R&B - Disp	7.74%	67,666	6.77%	133	7.37%	54,316	6.36%	110	7.82%	53,663	1.29%	388	8.01%	53,418	2.13%	376	5.52%	71,773	0.00%	914
76, 84, 112, 116	B.2.19.8.1.2	P-9	2W Analog Loop Design/<10 circuits/Non-Dispatch/GA(%)	R&B - Disp	7.74%	67,665			7.37%	54,316			7.82%	53,663			8.01%	53,418			5.52%	71,773	0.00%	2
76, 84, 112, 116	B.2.19.8.2.1	P-9	2W Analog Loop Design/>=10 circuits/Dispatch/GA(%)	R&B - Disp	7.66%	274	0.00%	1	10.09%	226			10.14%	207	0.00%	2	6.67%	180			12.37%	194	0.00%	2
76, 84, 112, 116	B.2.19.8.2.2	P-9	2W Analog Loop Design/>=10 circuits/Non-Dispatch/GA(%)	R&B - Disp	7.66%	274			10.09%	226			10.14%	207			6.67%	180			12.37%	194		
76, 84, 112, 116	B.2.19.9.1.1	P-9	2W Analog Loop Non-Design/<10 circuits/Dispatch/GA(%)	R&B (POTS) excl SB Or	7.90%	65,904	0.00%	263	6.40%	84,719	0.00%	203	7.83%	51,996	0.00%	246	8.24%	51,700	0.00%	177	5.56%	71,159	0.00%	315
76, 84, 112, 116	B.2.19.9.1.4	P-9	2W Analog Loop Non-Design/<10 circuits/Dispatch In/GA(%)	R&B (POTS) excl SB Or	4.88%	222,820	0.00%	14	4.15%	208,428	0.00%	1			0.00%	1	4.48%	252,681						
76, 84, 112, 116	B.2.19.9.2.1	P-9	2W Analog Loop Non-Design/>=10 circuits/Dispatch/GA(%)	R&B (POTS) excl SB Or	8.00%	211	0.00%	6	11.24%	169	0.00%	2	12.87%	150	0.00%	2	8.89%	135	0.00%	1	12.87%	171	0.00%	5
76, 84, 112, 116	B.2.19.9.2.4	P-9	2W Analog Loop Non-Design/>=10 circuits/Dispatch In/GA(%)	R&B (POTS) excl SB Or	16.67%	6	0.00%	1	0.00%	8							0.00%	28						
76, 84, 112, 116	B.2.19.10.1.1	P-9	2W Analog Loop w/INP Design/<10 circuits/Dispatch/GA(%)	R&B - Disp	7.74%	67,665	0.00%	1	7.37%	54,316	0.00%	1	7.82%	53,663	0.00%	1	8.01%	53,418			5.52%	71,773		
76, 84, 112, 116	B.2.19.10.1.2	P-9	2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/GA(%)	R&B - Disp	7.74%	67,665			7.37%	54,316			7.82%	53,663			8.01%	53,418			5.52%	71,773		
76, 84, 112, 116	B.2.19.10.2.1	P-9	2W Analog Loop w/INP Design/>=10 circuits/Dispatch/GA(%)	R&B - Disp	7.66%	274			10.09%	226			10.14%	207			6.67%	180			12.37%	194		
76, 84, 112, 116	B.2.19.10.2.2	P-9	2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/GA(%)	R&B - Disp	7.66%	274			10.09%	226			10.14%	207			6.67%	180			12.37%	194		
76, 84, 112, 116	B.2.19.11.1.1	P-9	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/GA(%)	R&B (POTS) excl SB Or	7.90%	65,904	0.00%	2	6.40%	84,719	0.00%	7	7.83%	51,996	0.00%	6	8.24%	51,700	0.00%	6	5.56%	71,159	0.00%	3
76, 84, 112, 116	B.2.19.11.1.4	P-9	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch In/GA(%)	R&B (POTS) excl SB Or	4.88%	222,826	0.00%	1	4.15%	208,428					0.00%	2	4.48%	252,681						
76, 84, 112, 116	B.2.19.11.2.1	P-9	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/GA(%)	R&B (POTS) excl SB Or	8.00%	211	0.00%	1	11.24%	169			12.87%	150			8.89%	135			12.87%	171		
76, 84, 112, 116	B.2.19.11.2.4	P-9	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch In/GA(%)	R&B (POTS) excl SB Or	16.67%	6			0.00%	8							0.00%	28						
76, 84, 112, 116	B.2.19.12.1.1	P-9	2W Analog Loop w/LNP Design/<10 circuits/Dispatch/GA(%)	R&B - Disp	7.74%	67,665	6.41%	234	7.37%	54,316	7.75%	142	7.82%	53,663	5.02%	239	8.01%	53,418	1.79%	391	5.52%	71,773	15.38%	325
76, 84, 112, 116	B.2.19.12.1.2	P-9	2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/GA(%)	R&B - Disp	7.74%	67,665			7.37%	54,316			7.82%	53,663			8.01%	53,418			5.52%	71,773		
76, 84, 112, 116	B.2.19.12.2.1	P-9	2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/GA(%)	R&B - Disp	7.66%	274	0.00%	2	10.09%	226	0.00%	2	10.14%	207	0.00%	1	6.67%	180			12.37%	194	0.00%	1
76, 84, 112, 116	B.2.19.12.2.2	P-9	2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/GA(%)	R&B - Disp	7.66%	274			10.09%	226			10.14%	207			6.67%	180			12.37%	194		
76, 84, 112, 116	B.2.19.13.1.1	P-9	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/GA(%)	R&B (POTS) excl SB Or	7.90%	65,904	0.00%	471	6.40%	84,719	0.00%	383	7.83%	51,996	0.00%	335	8.24%	51,700	0.00%	230	5.56%	71,158	0.00%	168
76, 84, 112, 116	B.2.19.13.1.4	P-9	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch In/GA(%)	R&B (POTS) excl SB Or	4.88%	222,826			4.15%	208,428			4.25%	250,407	0.00%	270	4.48%	252,681						
76, 84, 112, 116	B.2.19.13.2.1	P-9	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/GA(%)	R&B (POTS) excl SB Or	8.00%	211	0.00%	23	11.24%	169	0.00%	18	12.67%	150	0.00%	18	8.89%	135	0.00%	12	12.87%	171	0.00%	3
76, 84, 112, 116	B.2.19.13.2.4	P-9	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch In/GA(%)	R&B (POTS) excl SB Or	16.67%	6			0.00%	8			0.00%	7	0.00%	3	0.00%	28						
76, 84, 112, 116	B.2.19.14.1.1	P-9	Other Design/<10 circuits/Dispatch/GA(%)	Design	4.97%	2,313	11.64%	438	5.08%	3,560	7.78%	219	5.00%	4,081	11.67%	180	5.81%	3,738	9.80%	51	3.33%	5,525	0.00%	158
76, 84, 112, 116	B.2.19.14.1.2	P-9	Other Design/<10 circuits/Non-Dispatch/GA(%)	Design	1.17%	854			1.42%	635			0.75%	2,850			0.85%	235			0.30%	329		
76, 84, 112, 116	B.2.19.14.2.1	P-9	Other Design/>=10 circuits/Dispatch/GA(%)	Design	0.00%	81	100.00%	1	1.61%	62			0.00%	70			0.00%	48			0.00%	63		
76, 84, 112, 116	B.2.19.14.2.2	P-9	Other Design/>=10 circuits/Non-Dispatch/GA(%)	Design	0.00%	72			0.00%	107			0.00%	36			0.00%	2			0.00%	6		
76, 84, 112, 116	B.2.19.15.1.1	P-9	Other Non-Design/<10 circuits/Dispatch/GA(%)	R&B	7.74%	67,665	0.00%	332	7.37%	54,316	0.00%	20	7.82%	53,663	0.00%	28	8.01%	53,418	0.00%	3	5.52%	71,773	0.00%	2
76, 84, 112, 116	B.2.19.15.1.2	P-9	Other Non-Design/<10 circuits/Non-Dispatch/GA(%)	R&B	4.66%	476,013	0.00%	56	4.29%	437,226	0.00%	8	4.34%	457,412	0.00%	12	4.38%	468,517	0.00%	3	4.16%	466,878	0.00%	18
76, 84, 112, 116	B.2.19.15.2.1	P-9	Other Non-Design/>=10 circuits/Dispatch/GA(%)	R&B	7.66%	274	0.00%	22	10.09%	228	0.00%	2	10.14%	207			6.67%	180			12.37%	194		
76, 84, 112, 116	B.2.19.15.2.2	P-9	Other Non-Design/>=10 circuits/Non-Dispatch/GA(%)	R&B	1.35%	370	0.00%	8	1.67%	180			0.00%	183			3.98%	251			1.42%	353		
76, 84, 112, 116	B.2.19.16.1.1	P-9	INP (Standalone)/<10 circuits/Dispatch/GA(%)	R&B (POTS)	7.90%	65,921			7.54%	52,887			7.83%	51,987			8.24%	51,700			5.56%	71,159		
76, 84, 112, 116	B.2.19.16.1.2	P-9	INP (Standalone)/<10 circuits/Non-Dispatch/GA(%)	R&B (POTS)	4.88%	473,365			4.31%	435,135	0.00%	2	4.36%	454,937	0.00%	3	4.40%	466,355			4.16%	467,632		
76, 84, 112, 116	B.2.19.16.2.1	P-9	INP (Standalone)/>=10 circuits/Dispatch/GA(%)	R&B (POTS)	8.96%	212			11.24%	169			12.67%	150			8.89%	135			12.87%	171		
76, 84, 112, 116	B.2.19.16.2.2	P-9	INP (Standalone)/>=10 circuits/Non-Dispatch/GA(%)	R&B (POTS)	8.33%	12			5.88%	17			0.00%	13			2.78%	36			0.00%	105		
76, 84, 112, 116	B.2.19.17.1.1	P-9	LNP (Standalone)/<10 circuits/Dispatch/GA(%)	R&B (POTS)	7.90%	65,921			7.54%	52,887			7.83%	51,987			8.24%	51,709			5.55%	71,159		
76, 84, 112, 116	B.2.19.17.1.2	P-9	LNP (Standalone)/<10 circuits/Non-Dispatch/GA(%)	R&B (POTS)	4.88%	473,365			4.31%	435,135			4.36%	454,937			4.40%	466,355			4.16%	467,632		
76, 84, 112, 116	B.2.19.17.2.1	P-9	LNP (Standalone)/>=10 circuits/Dispatch/GA(%)	R&B (POTS)	8.96%	212			11.24%	169			12.67%	150			8.89%	135			12.87%	171		
76, 84, 112, 116	B.2.19.17.2.2	P-9	LNP (Standalone)/>=10 circuits/Non-Dispatch/GA(%)	R&B (POTS)	8.33%	12			5.88%	17			0.00%	13			2.78%	36			0.00%	105		
76, 84, 112, 116	B.2.19.18.1.1	P-9	Digital Loop < DS1/<10 circuits/Dispatch/GA(%)	Digital Loop < DS1	9.19%	12,842	5.98%	535	1.88%	372	5.83%	463	0.00%	351	7.87%	640	0.00%	399	11.36%	352			0.00%	228
76, 84, 112, 116	B.2.19.18.1.2	P-9	Digital Loop < DS1/<10 circuits/Non-Dispatch/GA(%)	Digital Loop < DS1	3.42%	1,257			0.00%	480			0.00%	13			0.00%	4						
76, 84, 112, 116	B.2.19.18.2.1	P-9	Digital Loop < DS1/>=10 circuits/Dispatch/GA(%)	Digital Loop < DS1	14.29%	14																		
76, 84, 112, 116	B.2.19.18.2.2	P-9	Digital Loop < DS1/>=10 circuits/Non-Dispatch/GA(%)	Digital Loop < DS1																				
76, 84, 112, 116	B.2.19.19.1.1	P-9	Digital Loop >= DS1/<10 circuits/Dispatch/GA(%)	Digital Loop >= DS1	2.39%	376	8.72%	172	5.71%	70	7.55%	159	0.00%	35	17.11%	152	0.00%	45	8.32%	505	9.71%	103	5.74%	682
76, 84, 112, 116	B.2.19.19.1.2	P-9	Digital Loop >= DS1/<10 circuits/Non-Dispatch/GA(%)	Digital Loop >= DS1	0.29%	885			0.00%	35			0.00%	8			0.00%	9			7.32%	41		
76, 84, 112, 116	B.2.19.19.2.1	P-9	Digital Loop >= DS1/>=10 circuits/Dispatch/GA(%)	Digital Loop >= DS1	0.00%	22																		
76, 84, 112, 116	B.2.19.19.2.2	P-9	Digital Loop >= DS1/>=10 circuits/Non-Dispatch/GA(%)	Digital Loop >= DS1	0.00%	72																		
Resale			**Invoice Accuracy**																					
43, 60, 96	A.4.1	B-1	GA(%)	BST - State	97.69%	$379,831,830	99.65%	$5,787,276	98.49%	$380,584,327	99.93%	$6,065,260	98.39%	$377,533,029	99.84%	$6,189,294	98.52%	$374,865,109	99.77%	$6,782,234	97.29%	$368,051,310	98.75%	$6,534,318
UNE			Invoice Accuracy																					

11/29/2001

FL Exception

BellSouth Monthly Performance Summary

Georgia 2001

FL Exception				Benchmark / Analog	September Data BST Measure	September Data BST Volume	September Data CLEC Measure	September Data CLEC Volume	August Data BST Measure	August Data BST Volume	August Data CLEC Measure	August Data CLEC Volume	July Data BST Measure	July Data BST Volume	July Data CLEC Measure	July Data CLEC Volume	June Data BST Measure	June Data BST Volume	June Data CLEC Measure	June Data CLEC Volume	May Data BST Measure	May Data BST Volume	May Data CLEC Measure	May Data CLEC Volume
44, 89	B.4.1	B-1	GA(%)	BST - State	97.89%	$379,831,830	99.97%	$6,896,761	96.49%	$380,594,327	99.84%	$6,017,740	96.39%	$377,533,929	99.94%	$4,746,084	94.52%	$374,895,109	99.73%	$3,840,697	97.29%	$384,051,310	99.95%	$3,589,367
		Operations Support Systems - Pre-Ordering																						
		% Interface Availability - CLEC																						
97	D.1.1.7	OSS-2	TAG/Region(%)	>= 99.5%			99.99%				99.99%				99.95%				99.98%				99.99%	
		Average Response Interval - CLEC (TAG) BST Measure Includes Additional 2 Seconds																						
116	D.1.4.1.1	OSS-1	RSAG, by TN/Region (seconds)	RNS - RSAG, by TN + 2 Sec	2.88	2,821,884	1.44	233,416	2.94	385,484	1.87	242,529	2.88	3,237,126	2.32	187,104	2.82	1,212,504	1.86	132,711	2.77	1,482,246	1.29	94,881
116	D.1.4.1.2	OSS-1	RSAG, by TN/Region (seconds)	ROS - RSAG, by TN + 2 Sec	3.17	6,295	1.44	233,416	3.42	7,932	1.87	242,529	3.28	7,386	2.32	187,104	3.27	7,054	1.86	132,711	3.23	8,212	1.29	94,881
116	D.1.4.2.1	OSS-1	RSAG, by ADDR/Region (seconds)	RNS - RSAG, by ADDR + 2 Sec	2.94	7,404,181	1.75	41,388	3.09	9,824,554	2.07	50,495	3.60	9,808,835	2.50	72,880	3.84	4,294,858	1.83	436,474	2.91	3,898,503	1.07	543,884
116	D.1.4.2.2	OSS-1	RSAG, by ADDR/Region (seconds)	ROS - RSAG, by ADDR + 2 Sec	5.36	803,389	1.75	41,388	5.76	785,143	2.07	50,495	5.87	818,414	2.98	72,680	5.74	582,588	1.83	436,474	5.86	635,777	1.07	543,664
116	D.1.4.3.1	OSS-1	ATLAS - MLH/Region (seconds)	Diagnostic																				
116	D.1.4.3.2	OSS-1	ATLAS - MLH/Region (seconds)	Diagnostic																				
116	D.1.4.4.1	OSS-1	ATLAS - DID/Region (seconds)	Diagnostic			8.03	1							1.61	4			8.59	7			1.05	19
116	D.1.4.4.2	OSS-1	ATLAS - DID/Region (seconds)	Diagnostic			8.03	1							1.61	4			8.59	7			1.05	19
116	D.1.4.5.1	OSS-1	ATLAS - TN/Region (seconds)	RNS - ATLAS - TN + 2 Sec	2.95	888,511	1.70	7,185			2.21	9,202			1.73	10,874	5.12	349,603	1.38	4,670	3.47	386,477	1.23	5,323
116	D.1.4.5.2	OSS-1	ATLAS - TN/Region (seconds)	ROS - ATLAS - TN + 2 Sec	2.63	246,979	1.70	7,185	2.08	308,812	2.21	9,202	2.65	283,887	1.73	10,074	2.84	277,471	1.30	4,670	2.68	298,954	1.23	5,323
116	D.1.4.6.1	OSS-1	DSAP/Region (seconds)	RNS - DSAP + 2 Sec	2.74	1,192,930	2.06	88,880			2.34	80,088			2.26	65,865	2.88	364,859	2.19	382,836	2.83	880,899	1.65	475,493
116	D.1.4.6.2	OSS-1	DSAP/Region (seconds)	ROS - DSAP + 2 Sec	2.66	276,940	2.06	88,880	2.87	331,119	2.34	80,888	2.57	284,671	2.26	65,865	2.80	288,127	2.19	382,836	2.80	318,632	1.65	475,493
116	D.1.4.7.1	OSS-1	HAL/CRIS/Region (seconds)	RNS - CRSACCTS + 2 Sec	3.43	4,241,777	2.82	93,820	3.68	5,388,212	2.80	168,972	3.58	5,903,374	3.80	133,850	3.66	2,461,808	2.07	63,821	3.88	2,308,194	0.70	78,803
116	D.1.4.7.2	OSS-1	HAL/CRIS/Region (seconds)	ROS - CRSOCSR + 2 Sec	3.19	442,287	2.82	93,820	3.25	527,219	2.80	168,972	3.19	461,218	3.80	133,830	3.23	448,716	2.07	63,821	3.17	487,169	0.70	78,003
116	D.1.4.8.1	OSS-1	RNS/CRSEINT/Region(seconds)	RNS - CRSACCTS + 2 sec																	3.88	2,308,194	1.17	3,728
116	D.1.4.8.2	OSS-1	ROS/CRSEINT/Region(seconds)	ROS - CRSOCSR + 2 sec																	3.17	487,169	1.17	3,728
116	D.1.4.9.1	OSS-1	RNS/CRSECSRL/Region(seconds)	RNS - CRSACCTS + 2 sec													3.66	2,461,808	1.32	1,805	3.88	2,308,194	1.17	3,728
116	D.1.4.9.2	OSS-1	ROS/CRSECSRL/Region(seconds)	ROS - CRSOCSR + 2 sec													3.23	448,716	1.32	1,805	3.17	487,168	1.17	3,728
		Operations Support Systems - Maintenance and Repair																						
38	D.2.2.2	OSS-3	ECTA/Region(%)	>= 99.5%			100.00%				100.00%				100.00%				100.00%				100.00%	
		General - Flow Through																						
		% Flow Through Service Requests																						
86, 107, 121	F.1.1.1	O-3	Summary/Region(%)	Diagnostic			88.82%	236,181			87.42%	288,331			77.38%	270,151			82.84%	245,003			88.82%	273,073
86, 107, 121	F.1.1.2	O-3	Aggregate/Region(%)	Diagnostic			88.82%	236,181			87.42%	288,331			77.38%	270,191			82.84%	245,003			88.82%	273,073
86, 107, 121	F.1.1.3	O-3	Residence/Region(%)	>= 95%			90.39%	165,598			90.86%	194,854			81.70%	191,978			87.52%	182,216			90.16%	220,614
86, 107, 121	F.1.1.4	O-3	Business/Region(%)	>= 90%			88.47%	5,636			72.14%	7,478			60.60%	6,393			57.11%	6,824			60.15%	7,518
86, 107, 121	F.1.1.5	O-3	UNE/Region(%)	>= 85%			78.33%	84,940			80.82%	84,281			67.30%	71,782			70.70%	56,264			74.87%	44,941
		% Flow Through Service Requests - Achieved																						
86, 107, 121	F.1.2.1	O-3	Summary/Region(%)	Diagnostic			77.71%	263,672			77.50%	322,978			69.28%	301,723			74.30%	273,208			78.44%	302,288
86, 107, 121	F.1.2.2	O-3	Aggregate/Region(%)	Diagnostic			77.71%	263,672			77.50%	322,978			69.28%	301,723			74.30%	273,208			78.44%	302,288
86, 107, 121	F.1.2.3	O-3	Residence/Region(%)	Diagnostic			82.49%	181,451			82.87%	213,414			74.98%	209,224			80.57%	197,988			83.65%	237,784
86, 107, 121	F.1.2.4	O-3	Business/Region(%)	Diagnostic			88.03%	7,714			52.61%	10,212			42.94%	9,088			41.18%	9,053			42.21%	10,713
86, 107, 121	F.1.2.5	O-3	UNE/Region(%)	Diagnostic			88.95%	74,707			88.50%	99,352			57.91%	83,419			60.11%	66,160			62.58%	53,771
		% Flow Through Service Requests - LNP																						
86, 107, 121	F.1.3.1	O-3	Summary/Region(%)	>= 85%			86.98%	4,647			84.40%	4,679			88.36%	4,860			91.83%	8,854			90.65%	11,802
86, 107, 121	F.1.3.2	O-3	Aggregate/Region(%)	>= 85%			86.98%	4,647			84.40%	4,679			88.36%	4,860			91.83%	8,854			90.65%	11,802
86, 107, 121	F.1.3.3	O-3	Residence/Region(%)	Diagnostic																				
86, 107, 121	F.1.3.4	O-3	Business/Region(%)	Diagnostic																				
		General - Billing																						
		Usage Data Delivery Accuracy																						
83	F.9.1	B-3	Region(%)	Parity w Retail	99.89%	4,197	100.00%	15,090	99.88%	6,318	100.00%	16,563	100.00%	5,950	100.00%	15,556	99.65%	6,084	100.00%	14,967	100.00%	8,898	99.99%	14,237
		Usage Data Delivery Timeliness																						
13	F.9.2	B-5	Region(%)	Parity w Retail	97.37%	30,110	99.48%	239,187,848	98.80%	34,467	98.30%	254,987,840	98.95%	40,304	98.82%	220,452,420	97.38%	38,844	99.21%	193,986,433	97.40%	38,202	98.04%	187,984,470
		General - Change Management																						
12	F.10.6	CM-5	GA(%)	>= 97% w in 15 min			100.00%	24			100.00%	29			100.00%	32			100.00%	29			100.00%	32

BellSouth Implementation of CLEC Feature Requests 2001-2002

I. Background:

The BellSouth Change Control Process ("CCP") document[1] states that the CCP is designed to manage all change requests "that affect external users of BellSouth's Electronic Interface Applications, associated manual process improvements, performance or ability to provide service, including defect/expedite notification."

The Change Control Prioritization Process is the method used by CLECs and BellSouth to rank the importance of both CLEC- *and* BellSouth-initiated change requests. The Prioritization Process is outlined in the BellSouth CCP document[2]. CCP participants meet monthly for discussion of such issues as the status of recently submitted change requests, prioritization (when the CLECs agree to do so) of change requests that have been accepted as candidates to be considered for implementation, and to address other process and operational questions related to change requests.

BellSouth also utilizes an Internal Prioritization Process in conjunction with the CCP. The Internal Prioritization Process is outlined in the BellSouth CCP document[3] and includes – to confirm and ensure viabilities – a review of the rankings determined by the CLECs and BellSouth during the Prioritization Process described above.

Additional factors considered during the prioritization process are as follows:

i. Mandated regulatory changes that are due during the release cycle are prioritized first.

ii. Change Requests that have related features that are all required for implementation to complete a function are given a higher priority than standalone changes to ensure the most efficient use of resources.

iii. Change Requests that are necessary for proper system operation and stability are given a high priority.

Key CLEC-driven changes such as Telephone Number ("TN") versus Address Validation and Parsed Customer Service Record ("CSR") are being addressed in 2001, for November 2001 and January 2002 implementation.

An explanation of how the capacity was allocated in 2001 follows.

II. BellSouth Software Capacity Year 2001 Utilization

Of the total Year 2001 software capacity (i.e., total hours to develop, test & implement system features), approximately 40% was utilized to address CLEC requests submitted as state/federal

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[1] Page 16 of BellSouth's CCP document Version 2.6 (September 10, 2001), provided as Exhibit OSS-39 to William N. Stacy affidavit of October 2, 2001.

[2] Pages 33-34, 54, CCP document Version 2.6 (September 10, 2001)

[3] Pages 34, 42, CCP document Version 2.6 (September 10, 2001)

mandate and via the CCP prioritization process. The CLEC-driven allocation resulted in approximately $65,992,680 and 119,867 programming hours. The remaining 60% was spent (approximately) as follows: $38,918,760 and 69,372 programming hours for maintenance and defects, $10,152,720 and 18,289 programming hours for public switched network, and $54,147,840 and 98,962 programming hours for BellSouth-initiated requests, which also benefit the CLECs. Due to significant demand from mandates and system operations, the majority of the capacity was used in these two areas with approximately twice the demand from mandates over system operations. In all cases, these software changes (i.e., features) directly benefited the CLECs.

A. State/Federal Mandates: In addition to the CCP, CLECs can submit requests through regulatory channels such as the state utilities commissions. Generally, these CLEC requests have a date-specific requirement, and address such issues as ordering requirements for new services, industry standards, parity with BellSouth's retail operations, reporting requirements, or required new inter-company processes – and usually beyond the ability to resolve within the CCP.

Following are some examples where CLECs were provided additional features/functionality as a result of CLEC-driven mandates:

- New Test Environment – BellSouth created a test environment enabling CLECs to test new functionality. This test environment is referred to as CAVE (CLEC Application Verification Environment)[4].
- CSR Parsing (in production January 5, 2002) – Enables CLECs to further parse Customer Service Record information into separate fields.
- Line Splitting (in production January 5, 2002. Note: Line Sharing was implemented September 2000.) – Enables a CLEC to offer voice and a DLEC to offer data on the same facility.
- Loop Make-up and XDSL – Changes were implemented enabling CLECs to process LSRs for ADSL/HDSL- and Unbundled Copper Loop (UCL)-capable loops. Changes were implemented that enables CLECs to select/reserve facilities as part of the Loop Make-up inquiry. Changes were implemented to the pre-ordering process to allow CLECs to view account records.
- Calculate Due Date – In addition to providing the estimated due date, provides CLECs the ability to receive a calculated due date based upon the work force availability and the product/service interval.
- Electronic Flow-Through of Service Requests – Multiple releases included features enabling CLECs to process additional types of service requests mechanically via LENS, TAG and/or EDI.
- TN Versus Address Validation – Changes were implemented enabling a CLEC with the option of submitting a migration request using the telephone number to order rather than submitting a complete street address.
- Increased Time Interval Prior to Cancellation of a Local Service Request ("LSR") – Allows CLECs 30 days before a clarified LSR is cancelled for no activity. This provides a CLEC a longer period of time to discuss any changes required by its customer.

[4] Was CLECs' #1 CCP priority in 2000

BellSouth Implementation of CLEC Feature Requests 2001-2002

B. Proper System Operation: Infrastructure and maintenance changes to the wholesale operating systems and interfaces (i.e., LENS, TAG, EDI, LESOG and LNP) are changes that accomplish the following upgrades of operating system software:

- Application software that improves stabilization, response time;
- Installation of hardware that require the movement of applications or change to applications; or
- Defect correction that includes changes made to software when the application is not working as requested or designed. A defect is any unintended operation that is not properly responsive to the originator's request, or an unintended operation caused by a flawed implementation of the original software change request.

C. Ordering Processes: These features are changes that either a CLEC or BellSouth has identified as beneficial to improve the ordering process, and are prioritized by importance within the CCP process. Examples implemented include:

- Business rule Local Service Request (LSR) field usage changes (Example: CR0133/CR0371 – Telephone Number versus Address Validation)
- New functionality for an interface (Examples: CR0015 – LENS Act of C – Change basic class of service; CR0030 – UNE-to-UNE migrations)
- Change existing functionality for an interface (Examples: CR0091 – Add Desired Frame Due Time to the Firm Order Confirmation; CR0149 – Modify and re-send FOCs and clarifications; CR0373 – Migrations as specified – LNA of G)
- Electronic ordering of a product/service (Examples: CR0153 – Line Sharing; CR0359 – Electronic ordering of xDSL loops; CR0361 – Mechanize service inquiry process for xDSL loops)

While a major portion of the software capacity expended in 2001 was focused on adherence to state/federal mandates listed above and ensuring proper system operation, a significant amount of work from other sources did progress through the CCP, and software changes were implemented.

A summary of CCP implemented activities follows.

April 2000 – November 2001

Total number of mandate requests: 13
- Public Switched Network 5
- CLEC-initiated 8

Total number of BST requests: 26
- 15 software changes

Total number of CLEC requests: 32
- 22 software changes

Total number of defect requests: 117

BellSouth Implementation of
CLEC Feature Requests
2001-2002

- BST (59)
- CLEC (58)
- 72 software changes

III. BellSouth Planning for 2002 Forward:

In an effort to address CLEC and KPMG Third-Party Test concerns in the CCP about release resource planning, BellSouth is making the following proposal: BellSouth will allocate 40% of its annual release capacity for implementing CLEC change requests and/or CLEC-driven mandates. This should provide the CLECs the ability to more readily rely upon the CCP as their first option for submitting a change request, thus obviating the need for CLECs to seek relief via regulatory channels. The remaining 60% will be used for implementing public switched network mandates such as NPA overlays and Number Pooling (5-10%), defects and maintenance (approximately 25%), and the remaining 25-30% for BellSouth change requests that also benefit the CLECs. This allocation strategy results in greater release capacity being devoted to CLEC-initiated requests (including CLEC-driven mandates such as TN validation) than to BellSouth-initiated requests. BellSouth will provide information to the requesting CLECs as to whether BellSouth believes the requested change will require a small, medium, or large amount of resource allocation. BellSouth will provide such a preliminary estimate for each change request submitted for prioritization. BellSouth will also track the capacity per the above categories and provide a year-to-date percent capacity used for CLEC-initiated requests. BellSouth will provide this report on a quarterly basis, beginning with calendar year 2002.

Further, of the top fifteen pending prioritized CCP feature requests, BellSouth has committed to implementing at least five of these during releases planned for the first half of 2002. This is in addition to BellSouth's commitment to deploy other mandates that have previously been communicated. Those features are related to:

- Line Splitting
- Parsed CSR
- Enhancement of Service Inquiry for SL1, SL2 and DSO
- Pre-ordering for DS1 and ISDN
- Single "C" Ordering.

Accessible

Southwestern Bell

(ORDERING AND PROVISIONING) New Product Announcement – SWBT IDSL Capable Loop – Arkansas, Kansas, Missouri, Oklahoma, and Texas

Date: March 5, 2001

Number: **CLEC01-046**

Contact: Southwestern Bell Account Manager

Category: UNE

This Accessible letter replaces **CLEC01-043**, dated March 2, 2001 announcing the availability of the ISDN Digital Subscriber Line (IDSL) Capable Loop effective March 12, 2001 in the SWBT region. The IDSL-Capable Loop is a 2-wire digital loop facility that supports IDSL services. The loop will be provisioned to pass end user data rates up to 144 KBPS bonded signal.

The 2-wire IDSL-capable loop will have associated recurring and non-recurring charges that will be billed according to the rates and terms contained in each CLEC's interconnection agreement. Acceptance testing is part of the standard provisioning process for the IDSL-capable loop and the cost for this procedure will be included in the loop costs. Contract language is currently available to amend your interconnection agreement.

The due date interval for IDSL implementation will be according to the terms contained in each CLEC's interconnection agreement.

For CLECs with terms for IDSL Capable Loops, please use the following information for ordering IDSL Capable Loops:

- SPEC Code: UNBLTA
- Service Code: AGXU
- NC: ADPA
- NCI: 02QC5.OOS
- SECNCI: 02IS5

A cross connect element must be purchased by the CLEC to connect the IDSL Capable Loop to the CLEC's collocation arrangement. The cross connect currently used with the two-wire digital loop will be utilized for IDSL Loops.

Please refer any questions to your Southwestern Bell Account Manager.

Accessible

Southwestern Bell

(ORDERING AND PROVISIONING) New Product Announcement – SWBT IDSL Capable Loop – Arkansas, Kansas, Missouri, Oklahoma, Texas

Date: May 3, 2001

Number: **CLEC01-106**

Contact: Southwestern Bell Account Manager

Category: UNE

This Accessible letter corrects **CLEC01-046**, dated March 5, 2001 announcing the availability of the ISDN Digital Subscriber Line (IDSL) Capable Loop effective March 12, 2001 in the SWBT region. The IDSL-Capable Loop is a 2-wire digital loop facility that supports IDSL services. The loop will be provisioned to pass end user data rates up to 144 KBPS bonded signal.

For CLECs with terms for IDSL Capable Loops, please use the following information for ordering IDSL Capable Loops:

SPEC Code: UNBLNT

The rest of the information as provided on CLEC01-046 will remain the same.

Please refer any questions to your Southwestern Bell Account Manager.

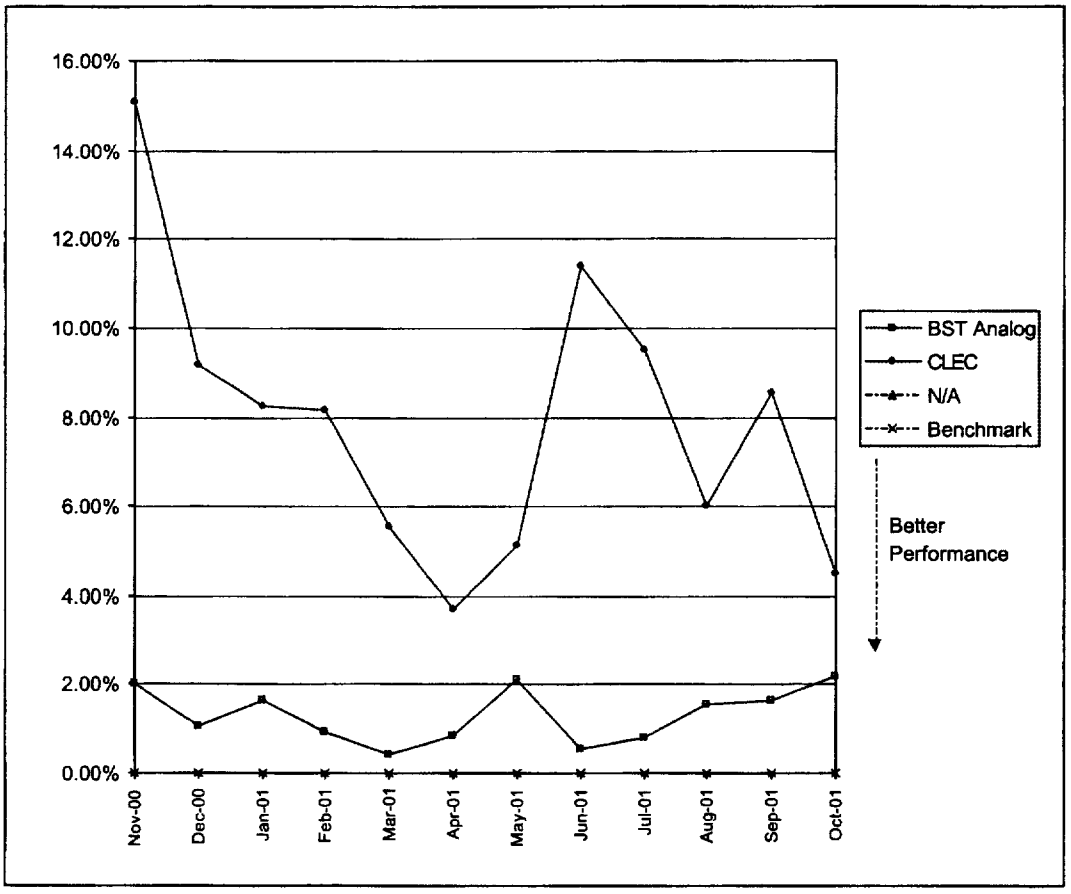
Unbundled Network Elements - Provisioning
% Provisioning Troubles within 30 Days
UNE ISDN/<10 circuits/Dispatch/GA (%)

(% of Trouble Reports Received within 30 Days of Service Order Completion)

Numerator indicates total number of initial trouble reports received within 30 days of service order completion for this disaggregation in the previous reporting period.

Volume indicates total number of service orders completed for this disaggregation in the previous reporting period.

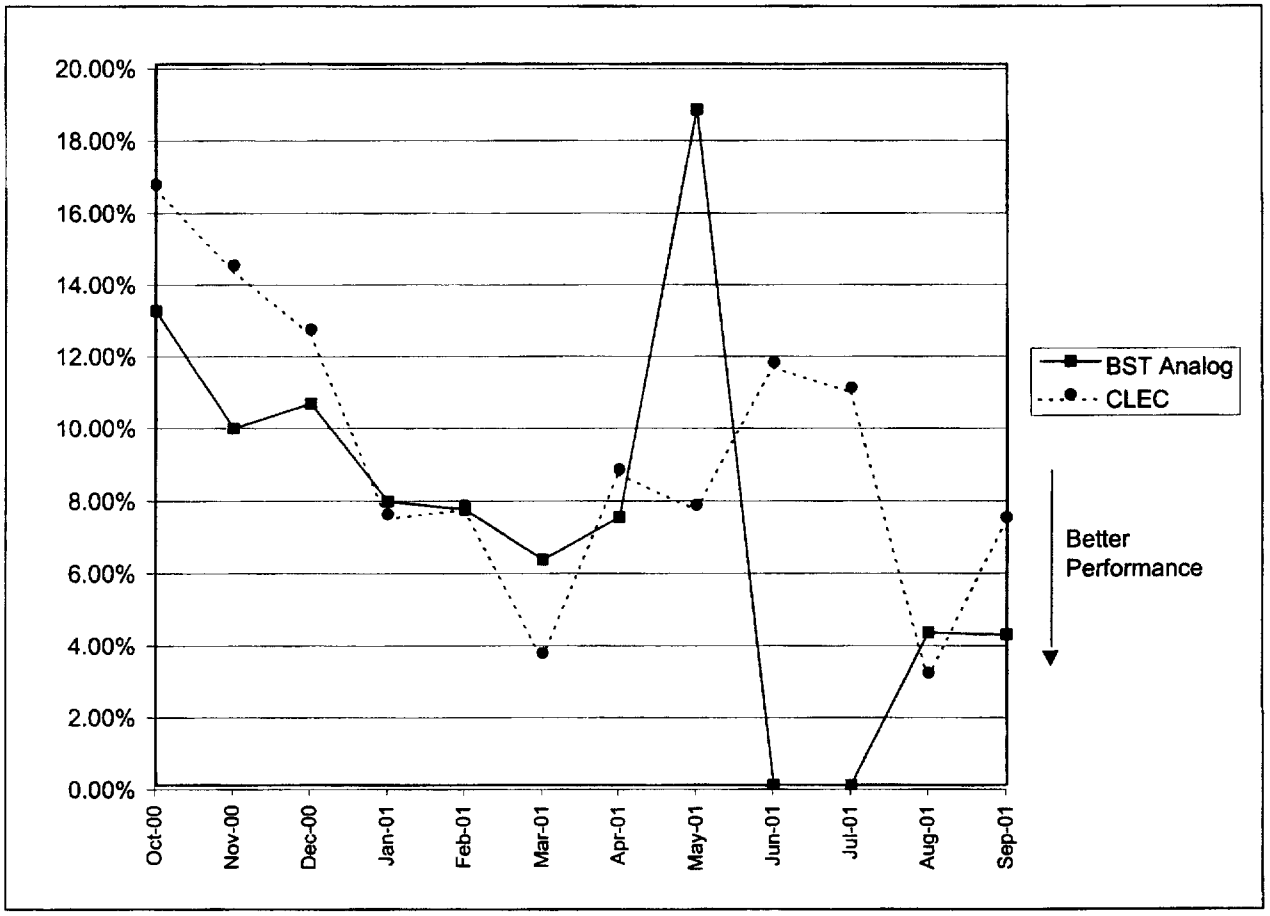
Retail ISDN - BRI data serves as the BST Analog for this report.



	Nov-00	Dec-00	Jan-01	Feb-01	Mar-01	Apr-01	May-01	Jun-01	Jul-01	Aug-01	Sep-01	Oct-01
BST Analog	2.03%	1.05%	1.65%	0.94%	0.42%	0.85%	2.11%	0.56%	0.79%	1.57%	1.66%	2.20%
Numerator	17	7	9	7	3	6	13	3	4	7	8	9
Volume	837	669	546	742	720	704	615	532	505	447	483	410
CLEC	15.09%	9.17%	8.24%	8.15%	5.56%	3.68%	5.11%	11.36%	9.48%	6.00%	8.50%	4.48%
Numerator	128	74	51	56	32	23	36	40	38	15	29	10
Volume	848	807	619	687	576	625	704	352	401	250	341	223
StDev												
StError	0.00687	0.00533	0.00748	0.00511	0.00362	0.00505	0.00793	0.00514	0.00593	0.00981	0.00903	0.01219
ZScore	-19.0068	-15.2356	-8.8112	-14.1119	-14.2176	-5.6091	-3.7819	-20.9912	-14.6463	-4.5221	-7.5860	-1.8776
Equity	NO	NO	NO	NO	NO	NO	NO	NO	NO	NO	NO	NO

Unbundled Network Elements - Provisioning
% Provisioning Troubles within 30 Days
UNE ISDN/<10 circuits/Dispatch/LA (%)
(% of Trouble Reports Received within 30 Days of Service Order Completion)

Numerator indicates total number of initial trouble reports received within 30 days of service order completion for this disaggregation in the previous reporting period.
Volume indicates total number of service orders completed for this disaggregation in the previous reporting period.
Retail ISDN - BRI data serves as the BST Analog for this report.



	Oct-00	Nov-00	Dec-00	Jan-01	Feb-01	Mar-01	Apr-01	May-01	Jun-01	Jul-01	Aug-01	Sep-01
BST Analog	13.16%	9.87%	10.56%	7.86%	7.63%	6.25%	7.41%	18.75%	0.00%	0.00%	4.26%	4.20%
Numerator	25	15	15	11	10	9	10	24	0	0	6	6
Volume	190	152	142	140	131	144	135	128	295	166	141	143
CLEC	16.67%	14.42%	12.63%	7.50%	7.76%	3.67%	8.74%	7.76%	11.70%	11.00%	3.13%	7.41%
Numerator	15	15	12	6	9	4	16	9	11	11	2	6
Volume	90	104	95	80	116	109	183	116	94	100	64	81
StDev												
StError	0.04326	0.03796	0.04073	0.03772	0.03385	0.03073	0.02972	0.05003	0.00000	0.00000	0.03042	0.02788
ZScore	-0.8114	-1.1988	-0.5082	0.0954	-0.0384	0.8395	-0.4475	2.1965			0.3715	-1.1519
Equity	YES	YES	YES	YES	YES	YES	YES	YES	NO	NO	YES	YES

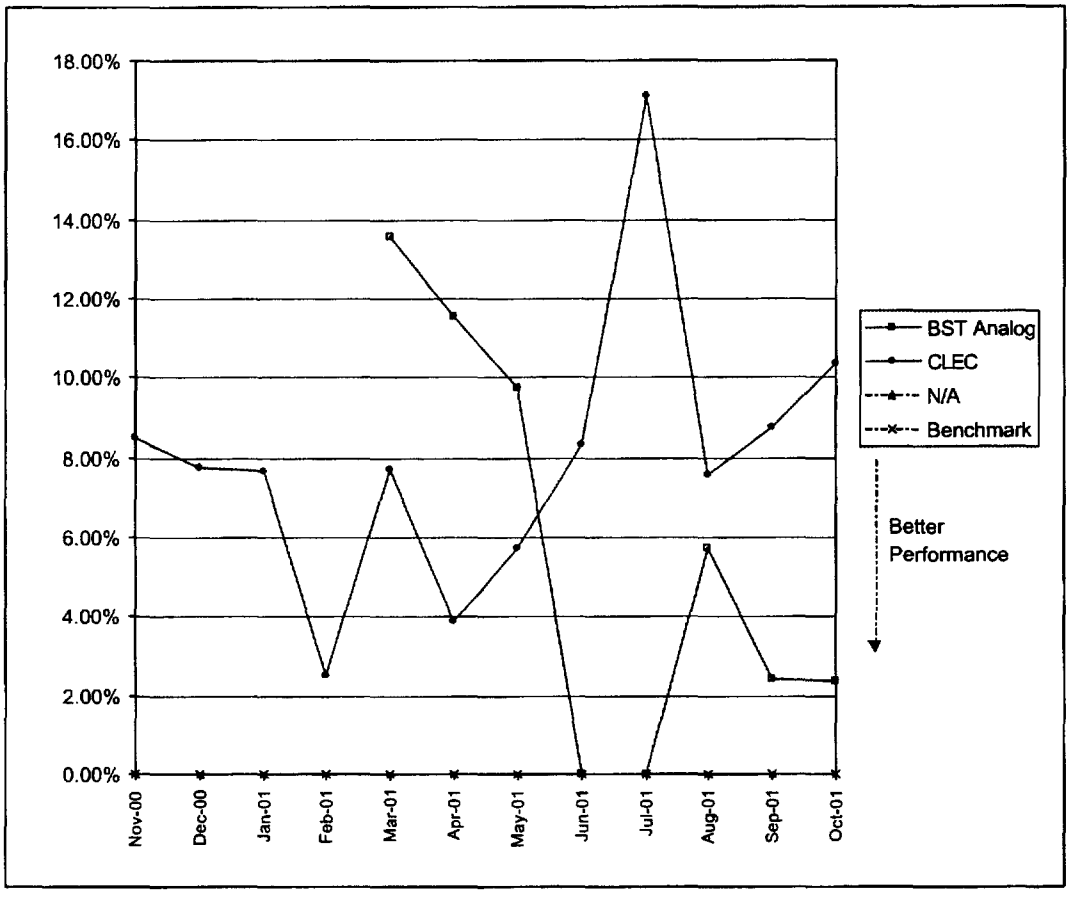
Unbundled Network Elements - Provisioning
% Provisioning Troubles within 30 Days
Digital Loop >= DS1/<10 circuits/Dispatch/GA (%)

(% of Trouble Reports Received within 30 Days of Service Order Completion)

Numerator indicates total number of initial trouble reports received within 30 days of service order completion for this disaggregation in the previous reporting period.

Volume indicates total number of service orders completed for this disaggregation in the previous reporting period.

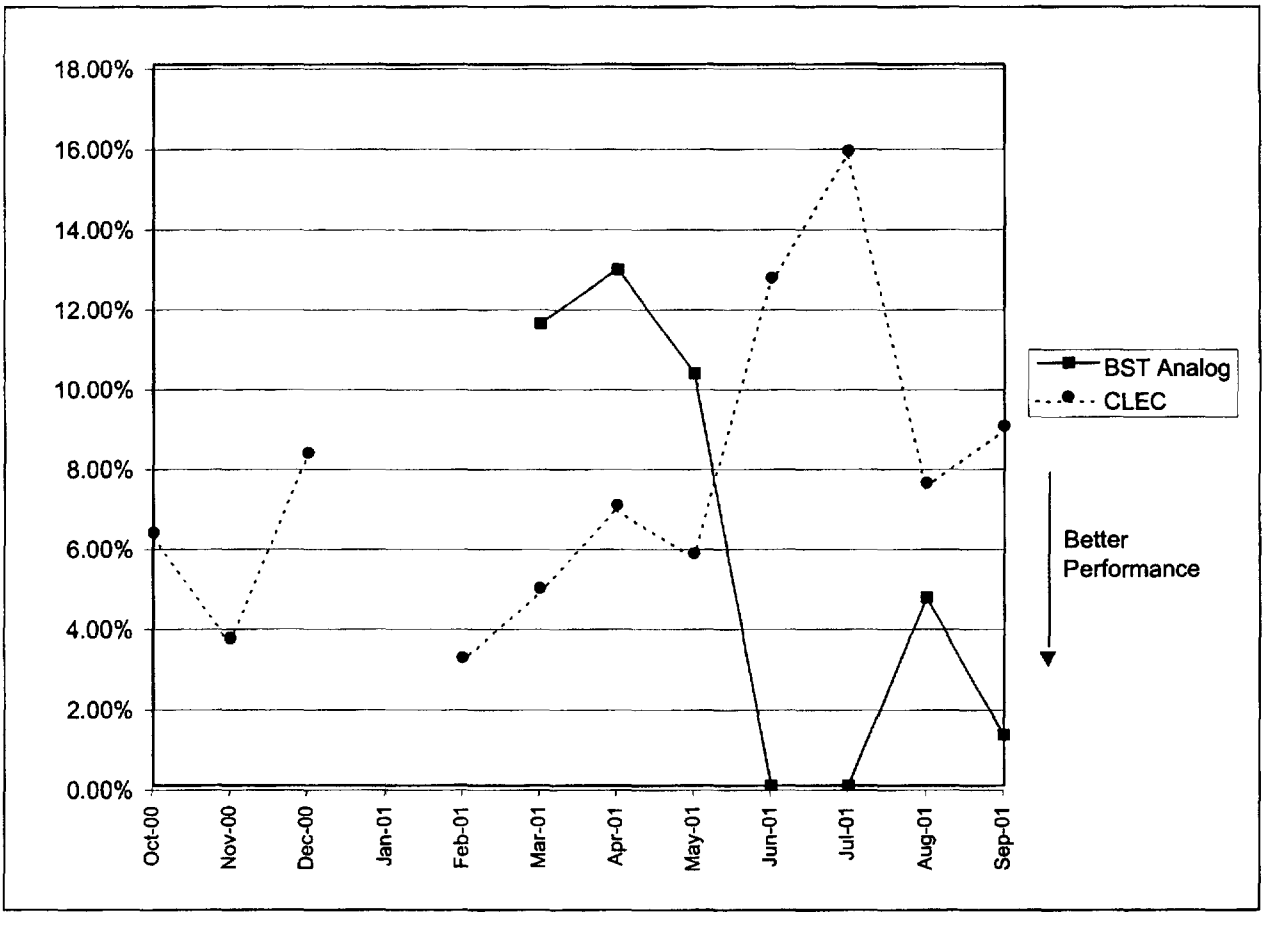
Retail Digital Loop >= DS1 data serves as the BST Analog for this report.



	Nov-00	Dec-00	Jan-01	Feb-01	Mar-01	Apr-01	May-01	Jun-01	Jul-01	Aug-01	Sep-01	Oct-01
BST Analog					13.58%	11.54%	9.71%	0.00%	0.00%	5.71%	2.39%	2.38%
Numerator					11	9	10	0	0	4	9	8
Volume					81	78	103	45	35	70	376	336
CLEC	8.49%	7.74%	7.64%	2.51%	7.69%	3.86%	5.74%	8.32%	17.11%	7.55%	8.72%	10.34%
Numerator	44	34	22	8	27	15	38	42	26	12	15	15
Volume	518	439	288	319	351	389	662	505	152	159	172	145
StDev												
StError					0.04223	0.03964	0.03136	0.00000	0.00000	0.03329	0.01407	0.01515
ZScore					1.3948	1.9375	1.2658			-0.5505	-4.4970	-5.2573
Equity					YES	YES	YES	NO	NO	YES	NO	NO

Unbundled Network Elements - Provisioning
% Provisioning Troubles within 30 Days
Digital Loop >= DS1/<10 circuits/Dispatch/LA (%)
(% of Trouble Reports Received within 30 Days of Service Order Completion)

Numerator indicates total number of initial trouble reports received within 30 days of service order completion for this disaggregation in the previous reporting period.
Volume indicates total number of service orders completed for this disaggregation in the previous reporting period.
Retail Digital Loop >= DS1 data serves as the BST Analog for this report.



	Oct-00	Nov-00	Dec-00	Jan-01	Feb-01	Mar-01	Apr-01	May-01	Jun-01	Jul-01	Aug-01	Sep-01
BST Analog						11.54%	12.90%	10.29%	0.00%	0.00%	4.69%	1.27%
Numerator						9	8	7	0	0	3	2
Volume						78	62	68	46	32	64	157
CLEC	6.31%	3.68%	8.30%		3.20%	4.92%	7.00%	5.79%	12.69%	15.84%	7.55%	8.97%
Numerator	19	13	20		8	13	25	19	42	16	12	14
Volume	301	353	241		250	264	357	328	331	101	159	156
StDev												
StError						0.04118	0.04612	0.04048	0.00000	0.00000	0.03129	0.01268
ZScore						1.6077	1.2793	1.1115			-0.9139	-6.0740
Equity						YES	YES	YES	NO	NO	YES	NO

Unbundled Network Elements - Maintenance and Repair

% Repeat Troubles within 30 Days

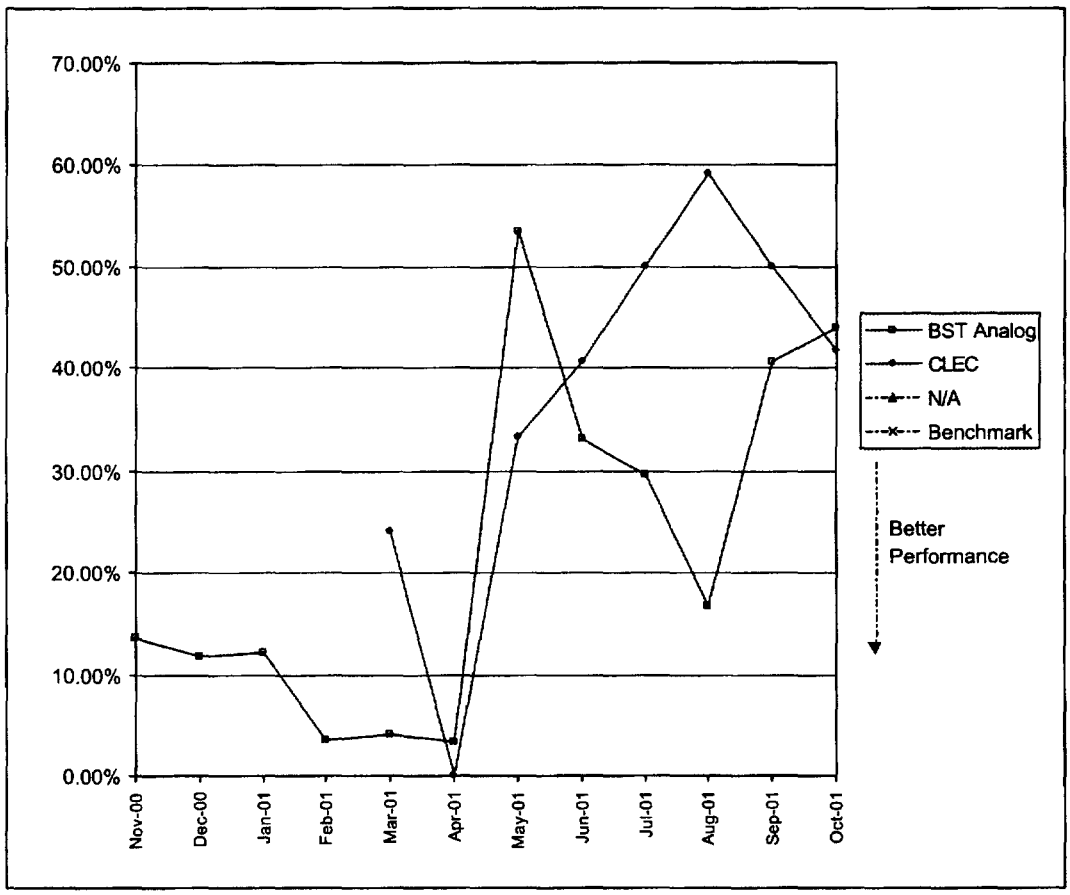
Line Sharing/Non-Dispatch/GA (%)

(% of Troubles that Repeated within 30 Days)

Numerator indicates total number of troubles that repeated within 30 days in the reporting period.

Volume indicates total trouble reports for this disaggregation closed in the reporting period.

ADSL Provided to Retail data serves as the BST Analog for this report.

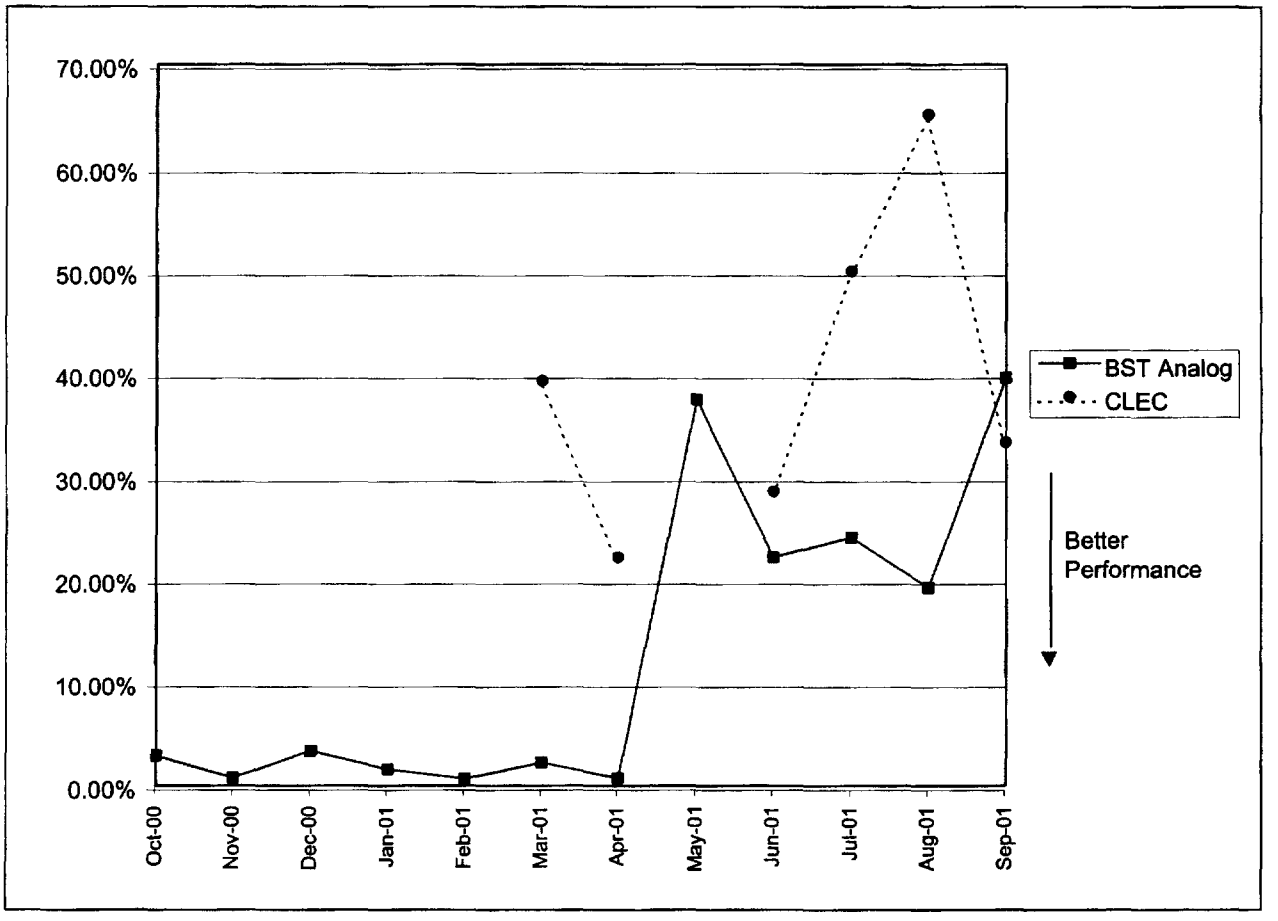


	Nov-00	Dec-00	Jan-01	Feb-01	Mar-01	Apr-01	May-01	Jun-01	Jul-01	Aug-01	Sep-01	Oct-01
BST Analog	13.51%	11.76%	12.09%	3.48%	3.97%	3.31%	53.51%	33.13%	29.61%	16.67%	40.60%	43.96%
Numerator	114	101	136	33	55	41	99	53	69	28	1,035	1,463
Volume	844	859	1,125	947	1,387	1,237	185	160	233	168	2,549	3,328
CLEC					24.14%	0.00%	33.33%	40.63%	50.00%	59.09%	50.00%	41.67%
Numerator					7	0	3	13	25	13	6	5
Volume					29	12	9	32	50	22	12	12
StDev												
StError					0.03663	0.05189	0.17025	0.09114	0.07116	0.08450	0.14210	0.14354
ZScore					-5.5057	0.6378	1.1853	-0.8229	-2.8649	-5.0208	-0.6612	0.1598
Equity					NO	YES	YES	YES	NO	NO	YES	YES

Effective April 2001: Analog Changed from 'Tariffed ADSL' to 'ADSL Provided to Retail'

Unbundled Network Elements - Maintenance and Repair
% Repeat Troubles within 30 Days
Line Sharing/Non-Dispatch/LA (%)
(% of Troubles that Repeated within 30 Days)

Numerator indicates total number of troubles that repeated within 30 days in the reporting period.
Volume indicates total trouble reports for this disaggregation closed in the reporting period.
ADSL Provided to Retail data serves as the BST Analog for this report.



	Oct-00	Nov-00	Dec-00	Jan-01	Feb-01	Mar-01	Apr-01	May-01	Jun-01	Jul-01	Aug-01	Sep-01
BST Analog	2.94%	0.76%	3.37%	1.59%	0.67%	2.23%	0.72%	37.50%	22.22%	24.14%	19.23%	39.63%
Numerator	4	3	14	8	2	10	3	21	14	28	15	363
Volume	136	394	415	504	297	448	419	56	63	116	78	916
CLEC						39.29%	22.22%		28.57%	50.00%	65.22%	33.33%
Numerator						11	2		2	6	15	4
Volume						28	9		7	12	23	12
StDev												
StError						0.02876	0.02848		0.16563	0.12976	0.09351	0.14212
ZScore						-12.8844	-7.5483		-0.3833	-1.9930	-4.9177	0.4430
Equity						NO	NO		YES	NO	NO	YES

Effective April 2001: Analog Changed from 'Tariffed ADSL' to 'ADSL Provided to Retail'

REVISED DUF ELEMENTS 11-26-01

		LOUISIANA WITH UPDATED FORECASTS (GEORGIA)
L.0	ACCESS DAILY USAGE FILE (ADUF)	
L.1	ACCESS DAILY USAGE FILE (ADUF)	
L.1.1	ADUF, Message Processing, per message	**$0.001825**
L.1.3	ADUF, Data Transmission (CONNECT:DIRECT), per message	**$0.00012147**
M.0	DAILY USAGE FILES	
M.1	ENHANCED OPTIONAL DAILY USAGE FILE	
M.1.1	Enhanced Optional Daily usage File: Message Processing, Per Message	**$0.229779**
M.2	OPTIONAL DAILY USAGE FILE	
M.2.1	Optional Daily Usage File: Recording, per Message	**$0.0000117**
M.2.2	Optional Daily Usage File: Message Processing, Per Message	**$0.002446**
M.2.3	Optional Daily Usage File: Message Processing, Per Magnetic Tape Provisioned	**$35.54**
M.2.4	Optional Daily Usage File: Data Transmission (CONNECT:DIRECT), Per Message	**$0.00010122**

422256

LOUISIANA ORDERED	GEORGIA PROPOSED FILED 10-1-01
$0.007983	$0.001849
$0.00012681	$0.00013189
$0.250015	$0.235679
$0.0000117	$0.0000088
$0.004641	$0.002496
$48.45	$35.76
$0.00010568	$0.00010991